



# **SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY**

**(AUTONOMOUS)**

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu)

(Accredited by NBA for Civil, EEE, Mech., ECE & CSE

Accredited by NAAC with 'A' Grade)

Puttur -517583, Chittoor District, A.P. (India)

## **DEPARTMENT OF COMPUTER SCIENCE & INFORMATION TECHNOLOGY**

### **BOARD OF STUDIES MINUTES OF MEETING**

<b>S. No</b>	<b>Date</b>	<b>BOARD OF STUDIES MINUTES OF MEETING</b>	<b>PageNo.</b>
1	31-05-2017	01	1-3
2	18-06-2018	02	4-9
3	27-12-2018	03	10-12
4	14-08-2019	04	13-17
5	28-08-2020	05	18-26
6	19-01-2021	06	27-31

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR  
(AUTONOMOUS)**

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**1<sup>st</sup> BoS Meeting of Computer Science & Information Technology (CSIT)**

Date: 31/05/2017

The 1<sup>st</sup> meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 31<sup>st</sup> May 2017 (Wednesday) in the Department of Computer Science & Information Technology, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the first BoS meeting and discussed about the following agenda:

**Agenda:**

1. Approval of course structure for I & II year UG in CSIT w.e.f., A.Y.2017-18.
2. Approval of syllabus for I & II year UG in CSIT w.e.f., A.Y.2017-18.
3. Approval of Panel of Question Paper setters.
4. Approval of Panel of Examiners.
5. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

**Minutes:**

**Agenda: 1**

Approval of course structure for I & II year UG in CSIT w.e.f., A.Y.2017-18.

**Resolution: 1**

After the detailed discussion the BoS resolved to approve the course structure for I & II year UG in CSIT (given in **Annexure –I**) and is applicable from the A.Y. 2017-18.

**Agenda: 2**

Approval of syllabus for I & II year UG in CSIT w.e.f., A.Y.2017-18.

**Resolution: 2**

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the I & II year of UG in CSIT (given in **Annexure –II**) and is applicable from the A.Y. 2017-18.

**Agenda: 3**

Approval of Panel of question paper setters.

**Resolution: 3**

Approved the panel of question paper setting (given in **Annexure –III**) to be submitted to the college academic council for approval.

**Agenda: 4**

Approval of Panel of examiners.

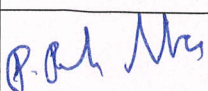
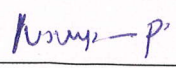
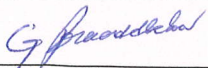
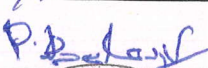
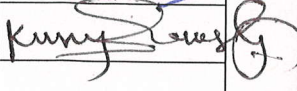
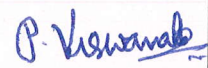




**Resolution: 4**

Approved the panel of examiners prepared for valuation (given in **Annexure –IV**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.



## Members Present

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Mr. P. Ramesh Babu	Associate Professor & HOD-SIETK	Chairman	
2.	Mrs. P. Nirupama	Professor - SIETK	Member	
3.	Mr. G. Prasad Babu	Associate Professor - SIETK	Member	
4.	Mr. P. Balaji	Associate Professor - SIETK	Member	
5.	Mr. K.V.S.K. Prakash	Assistant Professor - SIETK	Member	
6.	Prof. Viswanath .P	Professor, Indian Institute of Information Technology, Sri City, Chittoor District, Andhra Pradesh. Contact No: 7337324906 Email: viswanath.p@iiits.in	Member	
7.	Dr. K. Madhavi	Associate Professor & ACE – 3, Department of Computer Science & Engineering, JNTUA, Ananthapuramu PIN-517002 Contact No: 9440206501 Email: kasamadhavi@yahoo.com	Member	
8.	Dr. C. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: chapram@nitw.ac.in	Member	
9.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: ramamohansvu@yahoo.com	Member	
10.	Mr. E. Prakash	Senior Consultant, Deloitte Consulting Private Limited, Bangalore. Contact No: 9840370425 Email: prakashstar118@yahoo.co.in	Member	



**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR  
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**2<sup>nd</sup> BoS Meeting of Computer Science & Information Technology (CSIT)**

Date: 18/06/2018

The 2<sup>nd</sup> meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 18<sup>th</sup> June 2018 (Monday) in the Department of Computer Science & Information Technology, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the Second BoS meeting and discussed about the following agenda:

**Agenda:**

1. Approval of course structure for I & II year UG in CSIT w.e.f.,A.Y. 2018-19.
2. Approval of syllabus for I & II year UG in CSIT w.e.f.,A.Y. 2018-19.
3. Approval of Panel of Question Paper setters.
4. Approval of Panel of Examiners.
5. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

**Minutes:****Agenda: 1**

Approval of course structure for I & II year UG in CSIT w.e.f., A.Y. 2018-19.

**Resolution: 1**

After the detailed discussion the course structure for I & II year UG in CSIT was approved (given in **Annexure –I**) and is applicable from the A.Y., 2018-19.

**Agenda: 2**

Approval of syllabus for I & II year UG in CSIT w.e.f., A.Y. 2018-19.

**Resolution: 2**

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the I and II year B.Tech I & II-Semesters (given in **Annexure-II**).

**A. Course & Syllabus Comparison**

With reference to the R16 regulations, the new regulation (R18) syllabus for I<sup>st</sup> year and II<sup>nd</sup> year has the following modifications, which are given in the below table.

**I & II B.Tech**

S.No	R16 Regulation	R18 Regulation	Percentage of course content changed
1	Functional English	English	10
2	Engineering Mathematics- I	Mathematics-1	0
3	Engineering Chemistry	Chemistry	40
4	Computer Programming	Programming for Problem Solving	23.6
5	Engineering Graphics	Engineering Graphics & Design	0
6	English Language and Communication Skills Lab	English Lab	0
7	Engineering Chemistry Lab	Chemistry Lab	40
8	Computer Programming Lab	Programming for Problem	50

		Solving Lab	
9	Professional English		0
10	Engineering Mathematics-II	Mathematics-II	0
11	Engineering Physics	Semi-Conductor Physics	20
12	Human Values & Professional Ethics		0
13	Data Structures through C	Data Structures & Algorithms	11.8
14	Engineering Physics Lab	Physics Lab	0
15	Data Structures through C Lab	Data Structures & Algorithms Lab	6
16	Engineering & IT Workshop Lab	Workshop Practices Lab	0
17	Engineering Mathematics-III		0
18	Environmental Studies	Environmental Sciences	0
19	Advanced Data Structures through C++		0
20	Digital Logic Design	Digital Logic Design	0
21	Mathematical Foundations of Computer Science		0
22	Basic Electrical and Electronics Engineering	Basic Electrical Engineering	15
23	Advanced Data Structures through C++ Lab		0
25	Basic Electrical and Electronics Engineering Lab	Basic Electrical & Electronics Engineering Lab	0
26	Probability & Statistics	Probability & Statistics	0
27	Object Oriented Programming	Object Oriented Programming	10
28	Computer Organization	Computer Organization & Architecture	47.6
29	Database Management Systems	Database Management Systems	16.4
30	Fundamentals of Operating Systems	Fundamentals of Operating Systems	43.2
31	Object Oriented Programming Lab	Object Oriented Programming Lab	50
32	Database Management Systems Lab	Database Management Systems Lab	0
33	Fundamentals of Operating	Fundamentals of Operating	0



	Systems Lab	Systems Lab	
34	Ethical Hacking		0
35		Essence of Indian Traditional Knowledge	100
36		Analog Electronics Circuits	100
37		Formal Languages and Automata Theory	100
38		Indian Constitution	100
39		Discrete Mathematics	100
40		Biology for Engineers	100

### Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech I&II Year	40	30.73

### B. Course Relevance

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

Sno	Course Title	Course Code	Relevance
1	Engineering Graphics & Design	18ME0302	Skill Development
2	English	18HS0810	Skill Development
3	English Lab	18HS0811	Skill Development
4	Workshop Practices Lab	18ME0301	Skill Development
5	Programming for Problem Solving	18CS0501	Skill Development
6	Digital Logic Design	18CS0502	Skill Development
7	Basic Electrical Engineering	18EE0239	Skill Development
8	Programming for Problem Solving Lab	18CS0503	Skill Development
9	Essence of Indian Traditional Knowledge	18HS0817	Skill Development
10	Probability & Statistics	18HS0835	Skill Development
11	Analog Electronics Circuits	18EC0443	Skill Development
12	Data Structures & Algorithms	18CS0504	Skill Development
13	Computer Organization & Architecture	18CS0505	Skill Development
14	Database Management Systems	18CS0506	Skill Development
15	Data Structures & Algorithms Lab	18CS0507	Skill Development
16	Database Management Systems Lab	18CS0508	Skill Development
17	Basic Electrical & Electronics Engineering	18EE0241	Skill Development

	Lab		
18	Indian Constitution	18HS0816	Skill Development
19	Discrete Mathematics	18HS0836	Skill Development
20	Formal Languages and Automata Theory	18CS0509	Skill Development
21	Fundamentals of Operating Systems	18CI0601	Skill Development
22	Biology for Engineers	18HS0803	Skill Development
23	Object Oriented Programming	18CS0511	Skill Development
24	Fundamentals of Operating Systems Lab	18CI0602	Skill Development
25	Object Oriented Programming Lab	18CS0513	Skill Development
26	Comprehensive Online Examination – I	COE-1	Skill Development
27	Environmental Sciences	18HS0804	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

### **Agenda: 3**

Approval of Panel of question paper setters.

### **Resolution: 3**

Approved the panel of question paper setting (given in **Annexure –III**) to be submitted to the college academic council for approval.

### **Agenda: 4**

Approval of Panel of examiners.

### **Resolution: 4**

Approved the panel of examiners prepared for valuation (given in **Annexure –IV**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.

## Members Present

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Mr. P. Ramesh Babu	Associate Professor & HOD-SIETK	Chairman	P. Ramesh Babu
2.	Mrs. P. Nirupama	Professor - SIETK	Member	Nirupama - P.
3.	Mr. G. Prasad Babu	Associate Professor - SIETK	Member	G. Prasad Babu
4.	Mr. P. Balaji	Associate Professor - SIETK	Member	P. Balaji
5.	Mr. K.V.S.K. Prakash	Assistant Professor - SIETK	Member	K.V.S.K. Prakash
6.	Prof. Viswanath .P	Professor, Indian Institute of Information Technology, Sri City, Chittoor District, Andhra Pradesh. Contact No: 7337324906 Email: viswanath.p@iiits.in	Member	P. Viswanath
7.	Dr. K. Madhavi	Associate Professor & ACE – 3, Department of Computer Science & Engineering, JNTUA, Ananthapuramu PIN-517002 Contact No: 9440206501 Email: kasamadhavi@yahoo.com	Member	ABSENT
8.	Dr. C. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: chapram@nitw.ac.in	Member	ABSENT
9.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: ramamohansvu@yahoo.com	Member	A. Rama Mohan Reddy
10.	Mr. E. Prakash	Senior Consultant, Deloitte Consulting Private Limited, Bangalore. Contact No: 9840370425 Email: prakashstar118@yahoo.co.in	Member	E. Prakash



**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR**  
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**3<sup>rd</sup> BOS Computer Science & Information Technology (CSIT)**

Date: 27-12-2018

The 3<sup>rd</sup> meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 27<sup>th</sup> December, 2018 at 10:00 AM in the Department of Computer Science & Information Technology, Siddharth Institute of Engineering & Technology, Puttur, Chittoor – Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman BoS chaired the meeting and welcomed all the members to the third BoS meeting and discussed about the following agenda

**Agenda:**

1. Approval of course structure for III & IV year UG in CSIT w.e.f., A.Y.2019-20.
2. Approval of syllabus for III & IV year UG in CSIT w.e.f., A.Y.2019-20.
3. Approval of Panel of Question Paper setters.
4. Approval of Panel of Examiners.
5. Any other item

After a brief introduction the agenda items listed above were taken up for discussion and the following resolutions were passed.

**Agenda: 1**

Approval of course structure for III & IV year UG in CSIT w.e.f., A.Y.2019-20.

**Resolution: 1**

After detailed discussion the BoS resolved to approve the course structure for III & IV year UG in CSIT (given in **Annexure –I**) applicable from the A.Y.2019-20.

**Agenda: 2**

Approval of syllabus for III & IV year UG in CSIT w.e.f., A.Y.2019-20.

**Resolution: 2**

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the III and IV year B.Tech I & II-Semesters (given in **Annexure-II**)

**Agenda: 3**

Approval of Panel of Question Paper setters

**Resolution: 3**

Approved the panel of question paper setting (given in **Annexure –III**) to be submitted to the college academic council for approval.

**Agenda: 4**





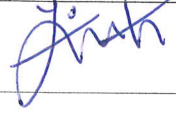

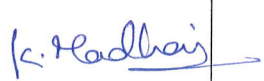



Approval of Panel of Examiners

**Resolution: 4**

Approved the panel of examiners prepared for valuation (given in **Annexure –IV**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.

**Members Present**

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Dr. P. Ramesh Babu	Professor & HOD-SIETK	Chairman	
2.	Mrs. P. Nirupama	Professor - SIETK	Member	
3.	Mr. G. Prasad Babu	Associate Professor - SIETK	Member	
4.	Mr. P. Balaji	Associate Professor - SIETK	Member	
5.	Mr. D. Sainath	Assistant Professor - SIETK	Member	
6.	Prof. Viswanath .P	Professor, Indian Institute of Information Technology, Sri City, Chittoor District, Andhra Pradesh. Contact No: 7337324906 Email: viswanath.p@iiits.in	Member	
7.	Dr. K. Madhavi	Associate Professor & ACE – 3, Department of Computer Science & Engineering, JNTUA, Ananthapuramu PIN-517002 Contact No: 9440206501 Email: kasamadhavi@yahoo.com	Member	
8.	Dr. C. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: chapram@nitw.ac.in	Member	
9.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: ramamohansvu@yahoo.com	Member	
10.	Mr. E. Prakash	Senior Consultant, Deloitte Consulting Private Limited, Bangalore. Contact No: 9840370425 Email: prakashstar118@yahoo.co.in	Member	



**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR**  
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**4<sup>th</sup> BoS Meeting of Computer Science & Information Technology (CSIT)**

Date: 14/08/2019

The 4<sup>th</sup> meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 14<sup>th</sup> August, 2019 (Wednesday) at 2.00 PM in the Department of Computer Science & Information Technology, Siddharth Institute of Engineering & Technology, Puttur, Chittoor – Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the fourth BoS meeting and discussed about the following agenda:

1. Approval of course structure for I year UG in CSIT w.e.f., A.Y. 2019-2020.
2. Approval of syllabi for I year UG in CSIT w.e.f., A.Y.2019-2020.
3. Approval of Panel of Question Paper setters.
4. Approval of Panel of Examiners.
5. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

**Minutes:****Agenda: 1**

Approval of course structure for I year UG in CSIT w.e.f., A.Y.2019-2020.

**Resolution: 1**

After detailed discussion the BoS resolved to approve the course structure for I year UG (given in **Annexure –I**) applicable from the A.Y.2019-2020.

**Agenda: 2**

Approval of syllabi for I year UG in CSIT w.e.f., A.Y.2019-2020.

**Resolution: 2**

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the I Year B.Tech I & II-Semesters (given in **Annexure-II**).

**A. Course & Syllabus Comparison**

With reference to the R18 regulations, the new regulation (R19) syllabus for I<sup>st</sup> year has the following modifications, which are given in the below table.

**I B.Tech**

S.No	R18 Regulation	R19 Regulation	Percentage of course content changed
1	Mathematics-1	Algebra and Calculus	60
2	Chemistry	Applied Chemistry	40
3	Engineering Graphics & Design	Engineering Graphics	10
4	English	Communicative English	10
5	Chemistry Lab	Applied Chemistry Lab	0
6	English Lab	Communicative English Lab	0
7	Workshop Practices Lab	Workshop Practices Lab	0

8	Mathematics-II		0
9	Semi-Conductor Physics	Semiconductor Physics	0
10	Programming for Problem Solving		63
11	Digital Logic Design	Digital Logic Design	0
12	Basic Electrical Engineering	Basic Electrical & Electronics Engineering	20
13	Programming for Problem Solving Lab	C and Data Structures Lab	85.7
14	Physics Lab	Semiconductor Physics Lab	0
15	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	0
16		Python Programming	100
17		Python Programming Lab	100

### Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech I Year	17	32.58

### B. Course Relevance

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

Sno	Course Title	Course Code	Relevance
1	Algebra and Calculus	19HS0830	Skill Development
2	Engineering Graphics	19ME0302	Skill Development
3	Python Programming	19CS0501	Skill Development
4	Python Programming Lab	19CS0502	Skill Development
5	Workshop Practices Lab	19ME0301	Skill Development
6	Probability & Statistics	19HS0835	Skill Development
7	Communicative English	19HS0810	Skill Development



8	Basic Electrical & Electronics Engineering	19EE0240	Skill Development
9	Digital Logic Design	19CS0503	Skill Development
10	Communicative English Lab	19HS0811	Skill Development
11	Essence of Indian Traditional Knowledge	19HS0817	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

**Agenda: 3**

Approval of Panel of question paper setters

**Resolution: 3**

Approved the panel of question paper setting (given in **Annexure –III**) to be submitted to the college academic council for approval.

**Agenda: 4**





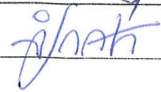

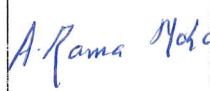

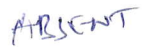

Approval of Panel of examiners

**Resolution: 4**

Approved the panel of examiners prepared for valuation (given in **Annexure –IV**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.

## Members Present

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Dr. P. Ramesh Babu	Professor & HOD-SIETK	Chairman	
2.	Dr. D. William Albert	Professor – SIETK	Member	
3.	Mr. P. Balaji	Associate Professor – SIETK	Member	
4.	Mrs. R. M. Mallika	Associate Professor – SIETK	Member	
5.	Mr. D. Sainath	Assistant Professor – SIETK	Member	
6.	Dr. D. Rajyalakshmi	Professor, Department of Computer Science & Engineering, JNTUK, Kakinada. Contact No.9618016555 E-mail : <a href="mailto:rajyalakshmi.cse@jntukucev.ac.in">rajyalakshmi.cse@jntukucev.ac.in</a>	Member	
7.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: <a href="mailto:ramamohansvu@yahoo.com">ramamohansvu@yahoo.com</a>	Member	
8.	Dr. Ch. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: <a href="mailto:chapram@nitw.ac.in">chapram@nitw.ac.in</a>	Member	
9.	Mr. D. Sriramdeshpande	Senior Project Engineer, Smart Enovation India Limited, Bangalore, Karnataka. Contact No. 8861345177, 9960506473 Email: <a href="mailto:sriram.d@smartenovations.com">sriram.d@smartenovations.com</a> <a href="mailto:sriramdeshpande@gmail.com">sriramdeshpande@gmail.com</a>	Member	
10.	V. Arun kumar	Technical Lead, HCL Technologies, Chennai. Contact No. 9176932956, Email: <a href="mailto:arunkumar_v12@yahoo.co.in">arunkumar_v12@yahoo.co.in</a>	Member	



**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR**  
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**5<sup>th</sup> BoS Meeting of Computer Science & Information Technology (CSIT)**

Date: 28/08/2020

The 5<sup>th</sup> meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 28<sup>th</sup> August, 2020 (Friday) at 10.00 AM in online through ZOOM App.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the 5<sup>th</sup> BoS meeting and discussed about the following agenda:

1. To discuss and frame the syllabi for II year B.Tech under R19 Regulation.
2. To discuss and frame the syllabi for III year B.Tech under R18 Regulation.
3. To prepare panel of examiners and paper setters for II and III B.Tech that comes under R19 & R18 respectively.
4. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.



**Minutes:****Agenda: 1**

To discuss and frame the syllabi for II year B.Tech under R19 Regulation.

**Resolution:1**

After the thorough discussion, syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the II-year B.Tech I & II- Semesters (given in **Annexure-I**)

**A. Course & Syllabus Comparison**

With reference to the R18 regulations, the new regulation (R19) syllabus for II<sup>nd</sup> year has the following modifications, which are given in the below table.

**II B.Tech**

S.No	R18 Regulation	R19 Regulation	Percentage of course content changed
1	Probability & Statistics		0
2	Analog Electronics Circuits		0
3	Data Structures & Algorithms	C and Data Structures	60
4	Computer Organization & Architecture	Computer Organization & Architecture	0
5	Database Management Systems	Database Management Systems	21.6
6	Data Structures & Algorithms Lab	C and Data Structures Lab	95
7	Database Management Systems Lab	Database Management Systems Lab	100
8	Basic Electrical & Electronics Engineering Lab		0
9	Indian Constitution	Indian Constitution	0
10	Discrete Mathematics	Discrete Mathematics	20

11	Formal Languages and Automata Theory	Formal Languages and Automata Theory	0
12	Fundamentals of Operating Systems	Principles of Operating Systems	5.2
13	Biology for Engineers		0
14	Object Oriented Programming	Object Oriented Programming through Java	28
15	Fundamentals of Operating Systems Lab	Principles of Operating Systems Lab	27
16	Object Oriented Programming Lab	Object Oriented Programming through Java Lab	0
17	Environmental Sciences	Environmental Science	0
18		Microprocessors & Microcontrollers	100
19		Water Technology	100
20		Generation of Energy Through Waste	100
21		Fundamentals of Mechanical Engineering	100
22		Introduction to Communication Systems	100
23		Management Science	100
24		Microcontroller and Applications Lab	100
25		Fundamentals of Urban Planning	100
26		Industrial Instrumentation	100
27		Mechanical Measurements & Control Systems	100
28		Elements of Embedded Systems	100
29		Intellectual Property Rights	100
30		Object oriented Analysis and design lab	100

**Consolidated Sheet**

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech II Year	30	63.72

**B. Course Relevance**

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

Sno	Course Title	Course Code	Relevance
1	Microprocessors & Microcontrollers	19EC0421	Skill Development
2	Computer Organization & Architecture	19CS0504	Skill Development
3	C and Data Structures	19CS0505	Skill Development
4	Database Management Systems	19CS0506	Skill Development
5	Water Technology	19CE0136	Skill Development
6	Generation of Energy Through Waste	19EE0238	Skill Development
7	Fundamentals of Mechanical Engineering	19ME0349	Skill Development
8	Introduction to Communication Systems	19EC0448	Skill Development
9	Management Science	19HS0813	Entrepreneurship
10	Microcontroller and Applications Lab	19EC0424	Skill Development
11	C and Data Structures Lab	19CS0507	Skill Development
12	Database Management Systems Lab	19CS0508	Skill Development
13	Indian Constitution	19HS0816	Skill Development
14	Discrete Mathematics	19HS0836	Skill Development
15	Formal Languages and Automata Theory	19CS0509	Skill Development
16	Object Oriented Programming through Java	19CS0510	Skill Development
17	Principles of Operating Systems	19CI0601	Skill Development
18	Fundamentals of Urban Planning	19CE0143	Skill Development
19	Industrial Instrumentation	19EE0233	Employability
20	Mechanical Measurements & Control Systems	19ME0350	Skill Development
21	Elements of Embedded Systems	19EC0449	Skill Development
22	Intellectual Property Rights	19HS0814	Skill Development
23	Object Oriented Programming through Java Lab	19CS0512	Skill Development
24	Principles of Operating Systems Lab	19CI0602	Skill Development
25	Object oriented Analysis and design lab	19CS0514	Skill Development
26	Environmental Science	19HS0805	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.



**Agenda: 2**

To discuss and frame the syllabi for III year B.Tech under R18 Regulation.

**Resolution:2**

After the thorough discussion, syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the III-year B.Tech I & II- Semesters (given in **Annexure-II**)

**A. Course & Syllabus Comparison**

With reference to the R16 regulations, the new regulation (R18) syllabus for III<sup>rd</sup> year has the following modifications, which are given in the below table.

**III B.Tech**

S.No	R16 Regulation	R18 Regulation	Percentage of course content changed
1	Computer Networks	Computer Networks	22
2	Formal Languages and Automata Theory		0
3	Python & R Programming	Python Programming	21.8
4	Data Warehousing and Data Mining	Data Warehousing and Data Mining	8
5	Managerial Economics and Financial Analysis	Managerial Economics and Financial Analysis	0
6	Design and Analysis of Algorithms	Design and Analysis of Algorithms	6.2
7	Computer Networks Lab		0
8	Data Mining, Python & R programming Lab	Python Programming Lab,	100
9	Data Mining, Python & R programming Lab	Data Mining Lab	0
10	Computer Graphics & Animation		0

11	Compiler Design	Compiler Design	42
12	Software Engineering & Testing	Software Engineering & Testing	85.2
13	Object Oriented Analysis and Design		0
14	Linux programming	Linux Programming	100
15	Software Testing and CASE tools Lab	Software Engineering & Testing Lab	100
16	Linux programming Lab		0
17	Advanced English Language and Communication Skills Lab		0
18		English for Corporate Communication Skills Lab	100
19		Analysis of Algorithms Lab	100
20		Web Technologies	100
21		Advanced Operating Systems	100
22		Internetworking with TCP/IP	100
23		Elements of Road Traffic Safety	100
24		Industrial Instrumentation	100
25		Non-Conventional Energy Resources	100
26		Introduction to IOT	100
27		Intellectual Property Rights	100
28		Web Technologies Lab	100

### Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech III Year	28	72.05

**B. Course Relevance**

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

<b>Sno</b>	<b>Course Title</b>	<b>Course Code</b>	<b>Relevance</b>
1	Managerial Economics and Financial Analysis	18HS0812	Entrepreneurship
2	Computer Networks	18CS0515	Skill Development
3	Design and Analysis of Algorithms	18CS0516	Skill Development
4	Python Programming	18CS0517	Skill Development
5	Software Engineering & Testing	18CI0603	Skill Development
6	Analysis of Algorithms Lab	18CS0518	Skill Development
7	Python Programming Lab	18CS0519	Skill Development
8	Software Engineering & Testing Lab	18CI0604	Skill Development
9	Aptitude Practices	18HS0842	Employability
10	Compiler Design	18CS0514	Skill Development
11	Data Warehousing and Data Mining	18CS0521	Skill Development
12	Web Technologies	18CS0523	Skill Development
13	Advanced Operating Systems	18CS0531	Skill Development
14	Linux Programming	18CS0532	Skill Development
15	Internetworking with TCP/IP	18CI0610	Skill Development
16	Elements of Road Traffic Safety	18CE0127	Skill Development
17	Industrial Instrumentation	18EE0234	Employability
18	Non-Conventional Energy Resources	18ME0307	Skill Development
19	Introduction to IOT	18EC0449	Skill Development
20	Intellectual Property Rights	18HS0814	Skill Development



21	Data Mining Lab	18CS0524	Skill Development
22	Web Technologies Lab	18CS0525	Skill Development
23	Mini Project	18CI0605	Entrepreneurship
24	Comprehensive Online Examination-II	COE-II	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

**Agenda: 3**

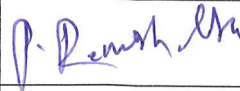




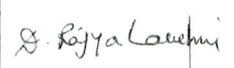

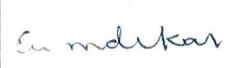


Approval of panel of examiners and paper setters for II and III B.Tech that comes under R19 & R18 respectively.

**Resolution:3**

Approved the panel of examiners and paper setters for II and III B.Tech that comes under R19 & R18 respectively (given in **Annexure –III**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of Thanks proposed by the Chairman-BoS.

**Members Present**

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Dr. P. Ramesh Babu	Professor & HOD-SIETK	Chairman	
2.	Mrs. P. Supriya	Associate Professor – SIETK	Member	
3.	Mr. D. Sainath	Associate Professor – SIETK	Member	
4.	Ms. N. Poornima	Assistant Professor – SIETK	Member	
5.	Mr. G. Venkatesh	Assistant Professor – SIETK	Member	
6.	Dr. D. Rajyalakshmi	Professor, Department of Computer Science & Engineering, JNTUK, Kakinada. Contact No.9618016555 E-mail : <a href="mailto:rajyalakshmi.cse@jntukucev.ac.in">rajyalakshmi.cse@jntukucev.ac.in</a>	Member	
7.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: <a href="mailto:ramamohansvu@yahoo.com">ramamohansvu@yahoo.com</a>	Member	
8.	Dr. Ch. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: <a href="mailto:chapram@nitw.ac.in">chapram@nitw.ac.in</a>	Member	
9.	Mr. D. Sriramdeshpande	Senior Project Engineer, Smart Enovation India Limited, Bangalore, Karnataka. Contact No. 8861345177, 9960506473 Email: <a href="mailto:sriram.d@smartenovations.co">sriram.d@smartenovations.co</a> <a href="mailto:msriramdeshpande@gmail.com">msriramdeshpande@gmail.com</a>	Member	
10.	V. Arun kumar	Technical Lead, HCL Technologies, Chennai. Contact No. 9176932956, Email: <a href="mailto:arunkumar_v12@yahoo.co.in">arunkumar_v12@yahoo.co.in</a>	Member	



**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR**  
**(AUTONOMOUS)**

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**6<sup>th</sup> BoS Meeting of Computer Science & Information Technology (CSIT)**

Date: 19/01/2021

The 6<sup>th</sup> meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 19<sup>th</sup> January, 2021 (Tuesday) at 10.30 AM in online through ZOOM App.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the 6<sup>th</sup> BoS meeting and discussed about the following agenda:

1. To discuss and frame the course structure & syllabi for I year B.Tech under R20 Regulation as per APSCHE suggestions.
2. To prepare panel of examiners and paper setters for I B.Tech that comes under R20.
3. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.



**Minutes:****Agenda: 1**

To discuss and frame the course structure & syllabi for I year B.Tech under R20 Regulation as per APSCHE suggestions.

**Resolution: 1**

After the thorough discussion, syllabus was framed to make the students acquire the required technical knowledge and skills. The BOS resolved to approve the R20 Regulation, Course Structure and syllabi for I year B.Tech (given in **Annexure –I**) applicable from the A.Y.2020-2021.

**A. Course & Syllabus Comparison**

With reference to the R19 regulations, the new regulation (R20) syllabus for I<sup>st</sup> year has the following modifications, which are given in the below table.

**I B.Tech**

S.No	R19 Regulation	R20 Regulation	Percentage of course content changed
1	Algebra and Calculus	Algebra and Calculus	30
2	Semiconductor Physics	Applied Physics	20
3	Basic Electrical & Electronics Engineering	Principles of Electrical Engineering	20
4	C and Data Structures	C Programming and Data Structures	0
5	Basic Electrical & Electronics Engineering	Basic Electronics Engineering	40
6	Probability & Statistics	Probability & Statistics	0
7	Applied Chemistry	Applied Chemistry	20
8	Communicative English	Communicative English	0
9	Engineering Graphics	Engineering Graphics	0
10	Digital Logic Design	Digital Logic Design	0
11	Python Programming		
12	Semiconductor Physics Lab	Applied Physics Lab	0
13		Basic Electrical and Electronics Engineering Lab	100

14	C and Data Structures Lab	C Programming and Data Structures Lab	12.5
15	Applied Chemistry Lab	Applied Chemistry Lab	10
16	Communicative English Lab	Communicative English Lab	0
17	Workshop Practices Lab	Workshop Practice Lab	0
18	Python Programming Lab		0

### Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech I Year	18	15.78125

### B. Course Relevance

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

Sno	Course Title	Course Code	Relevance
1	Principles of Electrical Engineering	20EE0250	Skill Development
2	C Programming and Data Structures	20CS0501	Skill Development
3	Basic Electronics Engineering	20EC0445	Skill Development
4	Basic Electrical and Electronics Engineering Lab	20EE0252	Skill Development
5	C Programming and Data Structures Lab	20CS0502	Skill Development
6	Communicative English	20HS0810	Skill Development
7	Engineering Graphics	20ME0301	Skill Development
8	Digital Logic Design	20CS0503	Skill Development
9	Communicative English Lab	20HS0811	Skill Development
10	Workshop Practice Lab	20ME0302	Skill Development
11	Indian Constitution	20HS0816	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

### Agenda: 2

To prepare panel of examiners and paper setters for I B.Tech that comes under R20.

**Resolution: 2**

Approved the panel of examiners and paper setters for I B.Tech that comes under R20 (given in **Annexure –II**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.



## Members Present

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Dr. P. Ramesh Babu	Professor & HOD-SIETK	Chairman	P. Ramesh Babu
2.	Mr. B. Rajakumar	Assistant Professor – SIETK	Member	B. Rajakumar
3.	Mr. G. Venkatesh	Associate Professor – SIETK	Member	G. Venkatesh
4.	Ms. N. Poornima	Assistant Professor – SIETK	Member	N. Poornima
5.	Mrs. D. Viswasahitya	Assistant Professor – SIETK	Member	D. Viswasahitya
6.	Dr. D. Rajyalakshmi	Professor, Department of Computer Science & Engineering, JNTUK, Kakinada. Contact No.9618016555 E-mail : <a href="mailto:rajyalakshmi.cse@jntukucev.ac.in">rajyalakshmi.cse@jntukucev.ac.in</a>	Member	D. Rajyalakshmi
7.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: <a href="mailto:ramamohansvu@yahoo.com">ramamohansvu@yahoo.com</a>	Member	A. Rama Mohan Reddy
8.	Dr. Ch. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: <a href="mailto:chapram@nitw.ac.in">chapram@nitw.ac.in</a>	Member	Ch. Sudhakar
9.	Mr. D. Sriramadeshpande	Senior Project Engineer, Smart Enovation India Limited, Bangalore, Karnataka. Contact No. 8861345177, 9960506473 Email: <a href="mailto:sriram.d@smartenovations.co">sriram.d@smartenovations.co</a> <a href="mailto:msriramadeshpande@gmail.com">msriramadeshpande@gmail.com</a>	Member	Sriram
10.	V. Arun kumar	Technical Lead, HCL Technologies, Chennai. Contact No. 9176932956, Email: <a href="mailto:arunkumar_v12@yahoo.co.in">arunkumar_v12@yahoo.co.in</a>	Member	V. Arun kumar