SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR

(AUTONOMOUS)

1ST BoS Meeting of Electronics and Communication Engineering (ECE)

Date: 08-07-2016

The 1st meeting of Board of Studies (BoS) in Electronics and Communication Engineering is held on 08 July 2016 at 1:30 PM in the Department of Electronics and Communication Engineering, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. M. Janardhana Raju, Chairman BoS chaired the meeting and welcomed all the members to the first BoS meeting and discussed about the following agenda

Agenda:

1. Preparation of course structure for UG & PG in ECE w.e.f., 2016-17.
2. Preparation of syllabi for I & II-year UG & PG in ECE w.e.f., 2016-17.
3. Preparation of syllabi for the subjects offered to various branches w.e.f., 2016-17.
4. Suggesting Panel of Question Paper setters.
5. Suggesting Panel of Examiners.
6. Methodologies for Innovative teaching.
7. Academic activities.

After a brief introduction the agenda items listed above were taken up for discussion and the following resolutions were passed.

Minutes:

Agenda: 1

Preparation of course structure for UG & PG in ECE w.e.f., 2016-17.

Resolution: 1

After detailed discussion the course structure for UG & PG in ECE is prepared (given in **Annexure –I**) and is applicable from the A.Y. 2016-17.

Agenda: 2

Preparation of syllabi for I & II-year UG & PG in ECE w.e.f., 2016-17.

Resolution: 2

After the thorough discussion syllabi was framed to make the students to acquire the required technical knowledge and skills. The syllabi framed for the I & II-year UG & PG in ECE (given in **Annexure-II**) and is applicable from the A.Y. 2016-17.

A. Course & Syllabus Comparison

With reference to the R15 regulations, the new regulation (R16) syllabus for Ist year has the following modifications, which are given in the below table.

I & II B.Tech

S.No	R15 Regulation	R16 Regulation	Percentage of course content changed
1.	Functional English	Functional English	0
2.	Mathematics – I	Engineering Mathematics-I	0
3.	Engineering Chemistry	Engineering Chemistry	0
4.	Computer Programming	Computer Programming	0
5.	Engineering Drawing	Engineering Graphics	20
6.	English Language Communication Skills Lab	English Language and Communication Skills Lab	0
7.	Engineering Chemistry Lab	Engineering Chemistry Lab	0
8.	Computer Programming Lab	Computer Programming Lab	0
9.	English for Professional Communication	Professional English	0
10.	Mathematics – II	Engineering Mathematics-II	0
11.	Engineering Physics	Engineering Physics	0
12.		Human Values & Professional Ethics	100
13.	Network Analysis	Network Analysis	0

14.	Engineering Physics Lab	Engineering Physics Lab	0
15.	Network Analysis Lab	Network Analysis Lab	0
16.	Engineering and IT Workshop	Engineering & IT workshop Lab	0
17.	Mathematics-III	Engineering Mathematics-III	0
18.	Electronic Devices and Circuits	Basic Electronic Devices	0
19.	Switching Theory and Logic Design	Switching Theory & Logic Design	0
20.	Signals and Systems	Signals and Systems	0
21.	Probability Theory and Stochastic Processes	Random Signal & Stochastic Processes	0
22.	Environmental Studies	Environmental Studies	20
23.	Electronic Devices and Circuits Laboratory	Basic Electronic Devices Lab	0
24.	Electrical Technology and Basic Simulation Laboratory	Basic Simulation Lab	0
25.		Data Structures through C	100
26.	Electronic Circuit Analysis	Electronic Circuit Analysis	0
27.	Computer Organization	Computer Organization and Architecture	0
28.	Electromagnetic Theory and Transmission Lines	Electromagnetic Theory and Transmission Lines	0
29.		Pulse & Digital Circuits	100
30.	Electrical Technology	Electrical Technology	0
31.	Electronic Circuit Analysis Laboratory	Electronic Circuit Analysis Lab	0
32.		Pulse & Digital Circuits Lab	100
33.	Electrical Technology and Basic Simulation Laboratory	Electrical Technology Lab	0
34.		Comprehensive Soft Skills	100

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECE B.Tech I& II Year	**34**	**15.88**

I & II M.Tech

S.No	R15 Regulation	R16 Regulation	Percentage of course content changed
1.	Digital System Design	Digital System Design	0
2.	Embedded System Concepts	Embedded System Concepts	0
3.	Advanced Digital Signal Processing	Advanced Digital Signal Processing	0
4.	Digital Communication Techniques	Digital Communication Techniques	0
5.	Adaptive Signal Processing	Adaptive Signal Processing	0
6.	Advanced Computer Architectures	Advanced Computer Architectures	0
7.	DSP Processors & Architectures	DSP Processors & Architectures	0
8.	Low Power VLSI Design	Low Power VLSI Design	0
9.	Digital System Design Lab	Digital System Design Lab	0
10.	Micro Computer System Design	Micro Computer System Design	20
11.	Hi-Speed Networks	Hi-Speed Networks	0
12.	Wireless Communications	Wireless Communications	0
13.	Coding Theory & Techniques	Coding Theory & Techniques	0
14.	Detection &Estimation of Signals	Detection & Estimation of Signals	0
15.	Image & Video Processing	Image & Video Processing	0
16.	Optical Communications	Optical Networks	100
17.	Compression Techniques	Compression Techniques	0
18.	Communications &Signal Processing Lab	Communications & Signal Processing Lab	0
19.	Seminar	Seminar	0
20.	Project work	Project work	0
21.	Advanced DSP & Applications	Advanced DSP & Applications	0
22.	Embedded System Concepts	Embedded System Concepts	0
23.	Advanced Computer Architecture	Advanced Computer Architecture	0
24.	Micro Controllers & Interfacing	Micro Controllers & Interfacing	0
25.	Operating Systems		0
26.	Digital IC Design	Digital IC Design	0
27.	VLSI Technology	VLSI Technology	0
28.	Algorithms for VLSI Design Automation	Algorithms for VLSI Design Automation	0

29.	Microcontrollers & Interfacing Lab	Microcontrollers & Interfacing Lab	100
30.	Testing & Testability	Testing & Testability	0
31.	Real Time Operating Systems	Real Time Operating Systems	0
32.	Hardware Software Co-design	Hardware Software Co-Design	0
33.	FPGA Architecture & Applications	FPGA Architecture and Applications	0
34.	Cryptography & Network Security	Cryptography & Network Security	0
35.	Radio Frequency Identification	Radio Frequency Identification	0
36.	Micro Electromechanical Systems	Micro Electromechanical Systems	0
37.	Expert Systems		0
38.	RTOS & FPGA Lab	RTOS and FPGA Lab	0
39.	Seminar	Seminar	0
40.	Project work	Project work	0
41.		System Modeling & Simulation	100
42.	VLSI Technology	VLSI Technology	0
43.	Analog IC Design	Analog IC Design	0
44.	Digital IC Design	Digital IC Design	0
45.	Hardware Description Languages	Verilog HDL	0
46.	Hardware Software Co-design	Hardware Software Co-Design	0
47.	Embedded system Concepts	Embedded system Concepts	90
48.	System Modeling & Simulation	System Modeling & Simulation	0
49.	ASIC Design	ASIC Design	0
50.	Digital IC Design Lab	Digital IC Design Lab	0
51.	Testing & Testability	Testing & Testability	0
52.	Low Power VLSI Design	Low Power VLSI Design	0
53.	Algorithms for VLSI Design Automation	Algorithms for VLSI Design Automation	0
54.	FPGA Architectures & Applications	FPGA Architectures & Applications	0
55.	Scripting Language for VLSI Design Automation	Scripting Language for VLSI Design Automation	0
56.	Nano Electronics	Nano Electronics	0
57.	Cryptography & Network	Cryptography & Network	0

	Security	Security	
58.	Real Time Operating Systems	Real Time Operating Systems	0
59.	Mixed Signal Lab	Mixed Signal Lab	0
60.	Seminar	Seminar	0

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
ECE M.Tech I& II Year	**60**	**6.833**

B. Course Relevance

The courses that come under the category of Employability, Skill or Entrepreneurship Development are shown in the table below.

I & II B.Tech

S.No	Course Title	Course Code	Relevance
1.	Functional English	16HS601	Skill Development
2.	English Language and Communication Skills Lab	16HS607	Skill Development
3.	Computer Programming Lab	16CS502	Skill Development
4.	Computer Organization and Architecture	16EC408	Skill Development
5.	Data Structures through C	16CS503	Skill Development
6.	Network Analysis	16EE205	Employability
7.	Network Analysis Lab	16EE206	Employability
8.	Basic Electronic Devices	16EC401	Employability
9.	Switching Theory & Logic Design	16EC402	Employability
10.	Signals and Systems	16EC403	Employability
11.	Random Signal & Stochastic Processes	16EC404	Employability
12.	Environmental Studies	16HS605	Employability

13.	Basic Electronic Devices Lab	16EC405	Employability
14.	Basic Simulation Lab	16EC406	Employability
15.	Electronic Circuit Analysis	16EC407	Employability
16.	Electromagnetic Theory and Transmission Lines	16EC409	Employability
17.	Pulse & Digital Circuits	16EC410	Employability
18.	Electrical Technology	16EE212	Employability
19.	Electronic Circuit Analysis Lab	16EC412	Employability
20.	Pulse & Digital Circuits Lab	16EC413	Employability
21.	Electrical Technology Lab	16EE213	Employability
22.	Computer Programming	16CS501	Employability
23.	Professional English	16HS610	Employability
24.	Network Analysis	16EE205	Employability
25.	Network Analysis Lab	16EE206	Employability
26.	Comprehensive Soft Skills	16HS614	Employability

I & II M.Tech

S. No	Course Title	Course Code	Relevance
1.	Digital System Design	16EC3801	Employability
2.	Advanced Digital Signal Processing	16EC3802	Employability
3.	Digital Communication Techniques	16EC3803	Employability
4.	Adaptive Signal Processing	16EC3804	Employability
5.	Embedded System Concepts	16EC5502	Employability
6.	Advanced Computer Architectures	16EC5503	Employability
7.	DSP Processors & Architectures	16EC3805	Employability
8.	Low Power VLSI Design	16EC5709	Employability

9.	Micro Computer System Design	16EC3807	Employability
10.	Image & Video Processing	16EC3808	Employability
11.	Wireless Communications	16EC3809	Employability
12.	Coding Theory & Techniques	16EC3810	Employability
13.	Detection & Estimation of Signals	16EC3811	Employability
14.	Hi-Speed Networks	16EC3812	Employability
15.	Optical Networks	16EC3813	Employability
16.	Compression Techniques	16EC3814	Employability
17.	Micro Controllers & Interfacing	16EC5501	Employability
18.	Embedded System Concepts	16EC5502	Employability
19.	Advanced Computer Architecture	16EC5503	Employability
20.	Advanced DSP & Applications	16EC5504	Employability
21.	Digital System Design	16EC3801	Employability
22.	Digital IC Design	16EC5703	Employability
23.	VLSI Technology	16EC5701	Employability
24.	Algorithms for VLSI Design Automation	16EC5710	Employability
25.	Real Time Operating Systems	16EC5506	Employability
26.	Testing & Testability	16EC5507	Employability
27.	Hardware Software Co-Design	16EC5508	Employability
28.	Cryptography & Network Security	16EC5509	Employability
29.	FPGA Architecture and Applications	16EC5708	Employability
30.	Radio Frequency Identification	16EC5510	Employability
31.	Micro Electromechanical Systems	16EC5511	Employability
32.	System Modeling & Simulation	16EC5705	Employability
33.	VLSI Technology	16EC5701	Employability
34.	Analog IC Design	16EC5702	Employability

35.	Digital IC Design	16EC5703	Employability
36.	Verilog HDL	16EC5704	Employability
37.	Hardware Software Co-Design	16EC5508	Employability
38.	Embedded system Concepts	16EC5502	Employability
39.	System Modeling & Simulation	16EC5705	Employability
40.	ASIC Design	16EC5706	Employability
41.	FPGA Architectures & Applications	16EC5708	Employability
42.	Testing & Testability	16EC5507	Employability
43.	Low Power VLSI Design	16EC5709	Employability
44.	Algorithms for VLSI Design Automation	16EC5710	Employability
45.	Scripting Language for VLSI Design Automation	16EC5711	Employability
46.	Nano Electronics	16EC5712	Employability
47.	Cryptography & Network Security	16EC5509	Employability
48.	Real Time Operating Systems	16EC5506	Employability

Modifications described above are carried out to the curriculum after discussions in the BoS by considering the feedback/suggestions from the stakeholders viz. students, alumni, faculty and employers.

Agenda: 3

Preparation of syllabi for the subjects offered to various branches w.e.f. 2016-17.

Resolution: 3

After the thorough discussion syllabi was prepared and finalized for the subjects offered to various branches (given in **Annexure-III**).

Agenda: 4

Suggesting Panel of Question Paper setters.

Resolution: 4

The panel of question paper setters was suggested (given in **Annexure –IV**).

Agenda: 5

Suggesting Panel of examiners

Resolution: 5

The panel of examiners was suggested (given in **Annexure –V**)

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of Thanks proposed by the Chairman-BoS.

Agenda: 6

Methodologies for Innovative teaching.

Resolution: 6

After the thorough discussion, innovative teaching methodologies like, ICT, smart book can be implemented in classroom teaching. Faculty and students are advised to learn advanced courses through nptel and other platforms.

Agenda: 7

Academic activities.

Resolution: 7

After the detailed discussion panel suggested to improve the quality of Academic activities and bring all of them under professional societies. Also instructed to improve the industry interactions.

Members Present

S.No.	Member Name	Academic/ Industry Position	Role in the BOS	Signature
1.	Prof. M.Janardhana Raju	Professor& HOD-SIETK	Chairman	
2.	Mrs. M.Kalpana	Associate Professor-SIETK (ES &VLSI)	Member	
3.	Mrs. K.S.Devesawari	Associate Professor-SIETK (Applied Electronics)	Member	
4.	Mr. C. Vijaya Bhaskar	Associate Professor-SIETK (VLSI system design)	Member	
5.	Mr. V.Viswanadha	Associate Professor-SIETK (DECS)	Member	
6.	Dr. S. Narayana Reddy	Professor, S.V. University, Tirupati	Member	
7.	Dr. Rama Komaragiri	Associate Professor, NIT Calicut	Member	ABSENT
8.	Dr.P.Ramana Reddy	Professor JNTUA,Ananthapuramu	Member	
9.	Mr. Narendra Reddy	Scientist-C, CMTI, Bangalore	Member	
10.	Mr. B.Venkatadri	Software Developer HCL Technologies	Member	