



**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR  
(AUTONOMOUS)**

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**6<sup>th</sup> BoS Meeting of Computer Science & Information Technology (CSIT)**

Date: 19/01/2021

The 6<sup>th</sup> meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 19<sup>th</sup> January, 2021 (Tuesday) at 10.30 AM in online through ZOOM App.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the 6<sup>th</sup> BoS meeting and discussed about the following agenda:

1. To discuss and frame the course structure & syllabi for I year B.Tech under R20 Regulation as per APSCHE suggestions.
2. To prepare panel of examiners and paper setters for I B.Tech that comes under R20.
3. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

**Minutes:****Agenda: 1**

To discuss and frame the course structure & syllabi for I year B.Tech under R20 Regulation as per APSCHE suggestions.

**Resolution: 1**

After the thorough discussion, syllabus was framed to make the students acquire the required technical knowledge and skills. The BOS resolved to approve the R20 Regulation, Course Structure and syllabi for I year B.Tech (given in **Annexure –I**) applicable from the A.Y.2020-2021.

**A. Course & Syllabus Comparison**

With reference to the R19 regulations, the new regulation (R20) syllabus for I<sup>st</sup> year has the following modifications, which are given in the below table.

**I B.Tech**

S.No	R19 Regulation	R20 Regulation	Percentage of course content changed
1	Algebra and Calculus	Algebra and Calculus	30
2	Semiconductor Physics	Applied Physics	20
3	Basic Electrical & Electronics Engineering	Principles of Electrical Engineering	20
4	C and Data Structures	C Programming and Data Structures	0
5	Basic Electrical & Electronics Engineering	Basic Electronics Engineering	40
6	Probability & Statistics	Probability & Statistics	0
7	Applied Chemistry	Applied Chemistry	20
8	Communicative English	Communicative English	0
9	Engineering Graphics	Engineering Graphics	0
10	Digital Logic Design	Digital Logic Design	0
11	Python Programming		
12	Semiconductor Physics Lab	Applied Physics Lab	0
13		Basic Electrical and Electronics Engineering Lab	100

14	C and Data Structures Lab	C Programming and Data Structures Lab	12.5
15	Applied Chemistry Lab	Applied Chemistry Lab	10
16	Communicative English Lab	Communicative English Lab	0
17	Workshop Practices Lab	Workshop Practice Lab	0
18	Python Programming Lab		0

### Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech I Year	18	15.78125

### B. Course Relevance

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

Sno	Course Title	Course Code	Relevance
1	Principles of Electrical Engineering	20EE0250	Skill Development
2	C Programming and Data Structures	20CS0501	Skill Development
3	Basic Electronics Engineering	20EC0445	Skill Development
4	Basic Electrical and Electronics Engineering Lab	20EE0252	Skill Development
5	C Programming and Data Structures Lab	20CS0502	Skill Development
6	Communicative English	20HS0810	Skill Development
7	Engineering Graphics	20ME0301	Skill Development
8	Digital Logic Design	20CS0503	Skill Development
9	Communicative English Lab	20HS0811	Skill Development
10	Workshop Practice Lab	20ME0302	Skill Development
11	Indian Constitution	20HS0816	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

### Agenda: 2

To prepare panel of examiners and paper setters for I B.Tech that comes under R20.

**Resolution: 2**

Approved the panel of examiners and paper setters for I B.Tech that comes under R20 (given in **Annexure –II**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.



## Members Present

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Dr. P. Ramesh Babu	Professor & HOD-SIETK	Chairman	P. Ramesh Babu
2.	Mr. B. Rajakumar	Assistant Professor – SIETK	Member	B. Rajakumar
3.	Mr. G. Venkatesh	Associate Professor – SIETK	Member	G. Venkatesh
4.	Ms. N. Poornima	Assistant Professor – SIETK	Member	N. Poornima
5.	Mrs. D. Viswasahitya	Assistant Professor – SIETK	Member	D. Viswasahitya
6.	Dr. D. Rajyalakshmi	Professor, Department of Computer Science & Engineering, JNTUK, Kakinada. Contact No.9618016555 E-mail : <a href="mailto:rajyalakshmi.cse@jntukucev.ac.in">rajyalakshmi.cse@jntukucev.ac.in</a>	Member	D. Rajyalakshmi
7.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: <a href="mailto:ramamohansvu@yahoo.com">ramamohansvu@yahoo.com</a>	Member	A. Rama Mohan Reddy
8.	Dr. Ch. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: <a href="mailto:chapram@nitw.ac.in">chapram@nitw.ac.in</a>	Member	Ch. Sudhakar
9.	Mr. D. Sriramdeshpande	Senior Project Engineer, Smart Enovation India Limited, Bangalore, Karnataka. Contact No. 8861345177, 9960506473 Email: <a href="mailto:sriram.d@smartenovations.co">sriram.d@smartenovations.co</a> <a href="mailto:msriramdeshpande@gmail.com">msriramdeshpande@gmail.com</a>	Member	Sriram
10.	V. Arun kumar	Technical Lead, HCL Technologies, Chennai. Contact No. 9176932956, Email: <a href="mailto:arunkumar_v12@yahoo.co.in">arunkumar_v12@yahoo.co.in</a>	Member	V. Arun kumar