SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR (AUTONOMOUS)

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2nd BoS Meeting of Computer Science & Information Technology (CSIT)

Date: 18/06/2018

The 2nd meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 18th June 2018 (Monday) in the Department of Computer Science & Information Technology, Siddharth Institute of Engineering & Technology, Puttur, Chittoor –Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the Second BoS meeting and discussed about the following agenda:

Agenda:

- 1. Approval of course structure for I & II year UG in CSIT w.e.f., A.Y. 2018-19.
- 2. Approval of syllabus for I & II year UG in CSIT w.e.f., A.Y. 2018-19.
- 3. Approval of Panel of Question Paper setters.
- 4. Approval of Panel of Examiners.
- 5. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

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Minutes:

Agenda: 1

Approval of course structure for I & II year UG in CSIT w.e.f., A.Y. 2018-19.

Resolution: 1

After the detailed discussion the course structure for I & II year UG in CSIT was approved (given in **Annexure** –**I**) and is applicable from the A.Y., 2018-19.

Agenda: 2

Approval of syllabus for I & II year UG in CSIT w.e.f., A.Y. 2018-19.

Resolution: 2

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the I and II year B.Tech I & II-Semesters (given in **Annexure-II**).

A. Course & Syllabus Comparison

With reference to the R16 regulations, the new regulation (R18) syllabus for I^{st} year and II^{nd} year has the following modifications, which are given in the below table.

S.No	R16 Regulation	R18 Regulation	Percentage of course content changed
1	Functional English	English	10
2	Engineering Mathematics- I	Mathematics-1	0
3	Engineering Chemistry	Chemistry	40
4	Computer Programming	Programming for Problem Solving	23.6
5	Engineering Graphics	Engineering Graphics & Design	0
6	English Language and Communication Skills Lab	English Lab	0
7	Er gineering Chemistry Lab	Chemistry Lab	40
8	Computer Programming Lab	Programming for Problem	50

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		Solving Lab	П
9	Professional English	7	0
10	Engineering Mathematics-II	Mathematics-II	. 0
11	Engineering Physics	Semi-Conductor Physics	20
12	Human Values & Professional Ethics	Human Values & Professional	
13	Data Structures through C	Data Structures & Algorithms	11.8
14	Engineering Physics Lab	Physics Lab	0
15	Data Structures through C Lab	Data Structures & Algorithms Lab	6
16	Engineering & IT Workshop Lab	Workshop Practices Lab	0
17	Engineering Mathematics-III		0
18	Environmental Studies	Environmental Sciences	0
19	Advanced Data Structures through C++		0
20	Digital Logic Design	Digital Logic Design	0
21	Mathematical Foundations of Computer Science		0
22	Basic Electrical and Electronics Engineering	Basic Electrical Engineering	15
23	Advanced Data Structures through C++ Lab		0
25	Basic Electrical and Electronics Engineering Lab	Basic Electrical & Electronics Engineering Lab	0
26	Probability & Statistics	Probability & Statistics	0
27	Object Oriented Programming	Object Oriented Programming	10
28	Computer Organization	Computer Organization & Architecture	47.6
29	Database Management Systems	Database Management Systems	16.4
30	Fundamentals of Operating Systems	Fundamentals of Operating Systems	43.2
31	Object Oriented Programming Lab	Object Oriented Programming Lab	50
32	Database Management Systems Lab	Database Management Systems Lab	0
33	Fundamentals of Operating	Fundamentals of Operating	0

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	Systems Lab	Systems Lab	
34	Ethical Hacking		0
35		Essence of Indian Traditional Knowledge	100
36		Analog Electronics Circuits	100
37		Formal Languages and Automata Theory	100
38		Indian Constitution	100
39		Discrete Mathematics	100
40		Biology for Engineers	100

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech I&II Year	40	30.73

B. Course Relevance

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

Sno	Course Title	Course Code	Relevance
1	Engineering Graphics & Design	18ME0302	Skill Development
2	English	18HS0810	Skill Development
3	English Lab	18HS0811	Skill Development
4	Workshop Practices Lab	18ME0301	Skill Development
5	Programming for Problem Solving	18CS0501	Skill Development
6	Digital Logic Design	18CS0502	Skill Development
7	Basic Electrical Engineering	18EE0239	Skill Development
8	Programming for Problem Solving Lab	18CS0503	Skill Development
9	Essence of Indian Traditional Knowledge	18HS0817	Skill Development
10	Probability & Statistics	18HS0835	Skill Development
11	Analog Electronics Circuits	18EC0443	Skill Development
12	Data Structures & Algorithms	18CS0504	Skill Development
13	Computer Organization & Architecture	18CS0505	Skill Development
14	Database Management Systems	18CS0506	Skill Development
15	Data Structures & Algorithms Lab	18CS0507	Skill Development
16	Database Management Systems Lab	18CS0508	Skill Development
17	Basic Electrical & Electronics Engineering	18EE0241	Skill Development

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	Lab	-	
18	Indian Constitution	18HS0816	Skill Development
19	Discrete Mathematics	18HS0836	Skill Development
20	Formal Languages and Automata Theory	18CS0509	Skill Development
21	Fundamentals of Operating Systems	18CI0601	Skill Development
22	Biology for Engineers	18HS0803	Skill Development
23	Object Oriented Programming	18CS0511	Skill Development
24	Fundamentals of Operating Systems Lab	18CI0602	Skill Development
25	Object Oriented Programming Lab	18CS0513	Skill Development
26	Comprehensive Online Examination – I	COE-1	Skill Development
27	Environmental Sciences	18HS0804	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

Agenda: 3

Approval of Panel of question paper setters.

Resolution: 3

Approved the panel of question paper setting (given in **Annexure –III**) to be submitted to the college academic council for approval.

Agenda: 4

Approval of Panel of examiners.

Resolution: 4

Approved the panel of examiners prepared for valuation (given in Annexure -IV) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.

Members Present

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Mr. P. Ramesh Babu	Associate Professor & HOD-SIETK	Chairman	p. p.h. Max
2.	Mrs. P. Nirupama	Professor - SIETK	Member	King - 1p.
3.	Mr. G. Prasad Babu	Associate Professor - SIETK	Member	6 Pestorla
4.	Mr. P. Balaji	Associate Professor - SIETK	Member	Englass
5.	Mr. K.V.S.K. Prakash	Assistant Professor - SIETK	Member	Kung Lenger
6.	Prof. Viswanath .P	Professor, Indian Institute of Information Technology, Sri City, Chittoor District, Andhra Pradesh. Contact No: 7337324906 Email: viswanath.p@iiits.in	Member	B. Viguesuelle
7.	Dr. K. Madhavi	Associate Professor & ACE – 3, Department of Computer Science & Engineering, JNTUA, Ananthapuramu PIN-517002 Contact No: 9440206501 Email: kasamadhavi@yahoo.com	Member	ABSENT
8.	Dr. C. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: chapram@nitw.ac.in	Member	ATKENT
9.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: ramamohansvu@yahoo.com	Member	A Cama Min Ki
10.	Mr. E. Prakash	Senior Consultant, Deloitte Consulting Private Limited, Bangalore. Contact No: 9840370425 Email: prakashstar118@yahoo.co.in	Member (Auther .