

FACULTY PROFILE

1. Name and Designation : V.Nisha Priyadharsini, Assistant professor
2. Date of Birth : 01.12.1996
3. Highest Qualification : M.E
4. Academic Performance (Descending Order):



S.No	Degree	University/ Institution	Year of Pass	% of Marks	Class
1	M.E(VLSI Design)	Sri venkateswara college of engineering and technology	2020	81	FIRST
2	B.E(Electronics and communication Engineering)	Prathyusha engineering collage	2018	71	FIRST
3	H.S.C	Bharathidasanar Mat. Hr. Sec. School, Arrakonam.	2014	72.16	FIRST
4	SSLC	T.K.V.M Mat. Hr. Sec. School, Tiruttani.	2012	91.2	FIRST

5. Total Years of teaching / industry experience till date : 02
6. Specialization : VLSI Design
7. Date of joining in this Department : 01/06/2021
8. Designation as on date of joining : ASSISTANT PROFESSOR
9. Present Designation : ASSISTANT PROFESSOR
10. Number of promotions since date of joining : NIL
11. Achievements as on 01.06.2022 :

S. No.	Achievements	
1	Number of papers published	2
2	Number of papers presented in conferences	NIL
3	Number of Projects guided	02
4	Number of FDPs / STTPs / Workshop Attended	05
5	Number of National / International Conferences Attended	NIL
6	Others (Book Chapters / Patents etc) :	1

14. Self - Appraisal:

Major Strengths	Major Weaknesses
1. Determination 2 .	1. Over thinking

V. Nisha

Signature of the faculty