**Regulation :** R13

### SIDDHARTH GROUP OF INSTITUTIONS :: PUTTUR

Siddharth Nagar, Narayanavanam Road – 517583

### **OUESTION BANK (DESCRIPTIVE)**

Subject with Code : Compiler Design (13A05502)

Year & Sem : III B.Tech & I-Sem

Course & Branch : B. Tech - CSE

UNIT-1

1.Explain the need for dividing the compilation process into various phases and explain<br/>itsfunctions. Explain how abstract stack machine can be used as translators.[L2,<br/>10M]

2. a) Describe the role performed by lexical analysis of the compiler	[L1, 5M]
b) Explain the need of code optimization in compiler	[L2, 5M]
3. Explain programming language basics in detail.	[L2, 10M]
4. a) Explain Symbol table management and error handling technique in compi	ler [L2, 5M]
b) Differentiate between compiler and interpreter.	[L4, 5M]
5. a)Explain construction tools in compiler	[L2, 5M]
b) Explain the different Language processor of a program.	[L2, 5M]
6. Define LEX. Explain the use and form of lex program with an example.	[L1, 10M]
7 Write short notes	[L6, 5+5M]
a) pass and phases of a compiler	
b) bootstrapping	
8. Explain briefly how to recognize tokens in lexical analysis.	[L2, 10M]
9. Write short notes	[L6, 5+5M]
a) Application of compiler technology	
b) Parts of compiler	
10 a)List the various phases of a compiler.	[L1, 2M]
b)Differentiate tokens, patterns, lexeme.	[L4, 2M]
c) List the operations on languages.	[L1, 2M]
d)Define Regular Expressions and Regular Grammar.	[L1, 2M]
e) List the various error recovery strategies for a lexical analysis.	[L1, 2M]

### UNIT-2

1.a) Construct the recursive decent parser for the string id\*(id+id) following grammar?

[L4, 5M]

$E \rightarrow E + T/T$	
$T \rightarrow T * F/F$	
F-> (E)/id	
b) Explain about Left factoring with an example?	[L2, 5M]
2. Define augmented grammar? Construct the LR(0) items for the following Gra	ammar?
S->L=R	[L1, 10M]
S->R	
L->*R	

L->id	
R->L	
3. Calculate first and follow for the following grammar?	[L3, 5M]
a) E->E+T/T	
T-> T*F/F	
F-> (E)/id	
b) S->xABC	[L3, 5M]
A->a bbD	
$B \rightarrow a   \varepsilon$	
$C \rightarrow b   \epsilon$	
$D \rightarrow c   \epsilon$	
4. Consider the grammar $E \rightarrow E + T, T \rightarrow T^*F, F \rightarrow (E)$ id. Using predictive parsing t	able parse the
string id+id*id.	[L3, 10M]
<ol> <li>Perform Shift Reduce Parsing for the input string (id*id+id) fr the following. E-&gt; E+E E*E (E) id</li> </ol>	[L3, 10M]
6. a) For the given grammar S->cAd , A->ab a , draw the parser tree for the inpu	t string w=cad
using recursive descent parsing with backtracking.	[L4, 5M]
b) For the given grammar T->dFa F->bg b , draw the parser tree for the input	string w=dba
using recursive descent parsing with backtracking.	[L4, 5M]
7. Consider the grammar	
S->AB ABad	
A->d	
E ->b	
D->b  ε	
B->c	
Derive the predictive parsing table. Show that the given grammar is LL(1) or not	[L3, 10M]
8. Consider the grammar S->xABC	
A->a bbD	
$B -> a \varepsilon$	
C->b  ε	
$D \rightarrow c   \epsilon$	
Derive the predictive parsing table.	[L3, 10M]
9. Perform Shift Reduce Parsing for the input string using the grammar.	[L4, 5+5M]
S->(L) a	
L->L,S S	
a) (a,(a,a))	
b) (a,a)	
10 a) What is phrase level error recovery?	[L1, 2M]
b) What are the different strategies of error recovery?.	[L1, 2M]
c) Define Left factoring.	[L1, 2M]
d) What is Shift – Reduce parsing?.	[L1, 2M]
e) What is ambiguous grammar?Give an example.	[L2, 2M]

# <u>UNIT-3</u>

1. Explain syntax directed definition.	[L2, 10M]
2. Describe the evaluation order of SDT with an example.	[L5, 10M]
3. Explain the type expression and type equivalence.	[L2, 10M]
4. Explain the Translation scheme of SDD.	[L2, 10M]
5. Describe the different representation of 3-address code with an example.	[L5, 10M]
6. Explain in detail about Backpatching Techniques?.	[L2, 10M]
7. Explain the applications of Syntax Directed Definition.	[L2, 10M]
8. Write down the translation procedure for control statement and switch statement	nt[L6, 10M]
9. Explain different types of intermediate code with an example.	[L2, 10M]
10 a) Define a syntax-directed translation.	[L1, 2M]
b) Define annotated parse tree.	[L1, 2M]
c) What are the three functions of backpatching?	[L1, 2M]
d) Write the Syntax of case statement?.	[L6, 2M]
e) Differentiate between L attribute and S attribute.	[L4, 2M]

## UNIT-4

1. Drav	v the format of Activation Record in stack allocation and explain each field	in it.
		[L4, 10M]
2. Expl	ain about Induction variable & Global data flow analysis.	[L2, 10M]
3. Expl	ain about the loop optimization techniques with an example.	[L2, 10M]
4. Defi	ne Symbol table. Explain different types of Data structure for symbol table	[L1, 10M]
5. Disti	nguish between static scope and dynamic scope. Briefly explain access to	non-local
names i	in static scope.	[L4, 10M]
6. Expl	ain the basic principles source of optimization.	[L2, 10M]
7. Expl	ain basic concept of static and dynamic storage allocation.	[L2, 10M]
8. Expl	ain heap management mechanism.	[L2, 10M]
9. Write	e briefly reference counting garbage collectors.	[L6, 10M]
10	a) Write any four algebraic simplification	[L6, 2M]
	b) Name any four procedural optimization techniques	[L6, 2M]
	c) Define scope and life time of variable.	[L1, 2M]
	d) Define symbol table.	[L1, 2M]
	e) What is meant by data flow equation?.	[L1, 2M]

# <u>UNIT-5</u>

1. Write about all issues in code generation. Describe it.	[L6, 10M]
2. Explain the target machine architecture?	[L2, 10M]
3. Write about code scheduling.	[L6, 10M]
4. Describe the various strategies in register allocation.	[L5, 10M]

QUESTION BANK 2016 5. Explain the peephole optimization?. [L2, 10M] 6. Construct the DAG for following statement. a+b\*c+d+b\*c [L3, 10M] 7. Construct the DAG for the following basic blocks [L3, 10M] 1. t1:=4\*i 2. t2:=a[t1] 3. t3:=4\*i 4. t4:=b[t3] 5. t5:=t2\*t4 6. t6:=prod+t5 7. prod:=t6 8. t7:=i+1 9. i:=t7 10. if i<=20 goto 1 8. Explain the simple code generator and generate target code sequence for the following statement d:=(a-b)+(a-c)+(a-c)[L2, 10M] 9. Write short notes on i)Simple code generator [L6, 5+5M] ii) Register allocation a) What is the role of peephole optimization in compilation process 10 [L1, 2M] b) Write the issues in the design of a code generator.(any 4) [L6, 2M] c) Give the variety of forms in target program [L1, 2M] d) Give the application of DAG. [L1, 2M] e) Define Dead-code elimination with example. [L1, 2M]

#### SIDDHARTH GROUP OF INSTITUTIONS :: PUTTUR Siddharth Nagar, Narayanavanam Road - 517583 **OUESTION BANK (OBJECTIVE) Subject with Code :** Compiler Design (13A05502) Course & Branch : B. Tech - CSE Year & Sem : III B.Tech & I-Sem **Regulation :** R13 UNIT-1 1. Popular type of intermediate code generation language 1 ſ A) 3 address code B) 33 address code C) 30 address code D) 333 address code 2. In code generation the optimizing code is converted into \_\_\_\_\_ 1 ſ B) Machine level language A) Assembly level language C) Mission code D) Both A & C 3. How many times the source code will be scanned is called ſ 1 A) Pass B) Phase C) Parse D) Scanner 4. The logical operation for each part of the process of compilation is called \_\_\_\_\_[ 1 A) Pass B) Phase

D) Scanner

C) Parse

5. Which is property of boot strapping? [ ] A) It must compile source language "s" B) It must use implementation language "i" C) It must generate target language "T" D) All the above 6. Cross compiler runs program in one program and does not produce target code for \_\_\_\_\_ 1 Γ A) Same machine B) Another machine C) Both A&B D) None 7. Which is not compiler construction tool \_\_\_\_\_ [ ] A) Parser B) Scanner generator D) None C) Data flow synthesis 8. Token means sequence of \_\_\_\_\_ ſ 1 A) Integers B) Floats C) Characters D) All the above 9. Low level programs are \_\_\_\_\_ to write 1 ſ A) Easier B) Harder C) Softer D) Light \_\_\_\_\_ is a grouping of declarations and statements 1 10. \_\_\_\_ ſ A) Scope B) Block C) Shelves D) Racks 11. \_\_\_\_ directly executes the operations specified in the source program on inputs supplied by the user. ſ 1 A) Interpreter B) Target program C) Machine language D) Assembly language 12. The \_\_\_\_\_ resolves external memory addresses. [ 1 **COMPILER DESIGN** Page 5

A) Translator C) Linker	B) Virtual machine		
,	D) Pre processor	impos	
13.The part breaks up the source pro	bgrain into constituent pieces and	r	-
grammatical structure on them.		L	J
A) Synthesis	B) Analysis		
C) Analytical	D) Syntax	r	-
4. Information about the source program and		[	]
A) Syntax table	B) Analytical table		
C) Symbol table	D) Synthetic table	F	-
15. The analysis part often called		[	]
A) Right end	B) Left end		
C) Back end	D) Front end	_	_
16. The first phase of compiler is called	_	[	]
A) Lexical analysis	B) Scanning		
C) Lexical scanning	D) Both A&B		
17. The lexical analyzer produces output in the		[	]
<ul><li>A) (token-name, attribute-value)</li><li>C) (attribute-value, token-name)</li></ul>	B) (token-value, attribute-n	ame)	
C) (attribute-value, token-name)	D) (attribute-name, token-w	value)	
18 gathers type information and saves it in			ole.
		[	]
A) Lexical analyzer	B) Syntax analyzer	-	-
C) Semantic analyzer	D) Analyzer		
19. An important part of semantic analyzer is _		[	]
A) Generating tokens	B) parser tree generation	L	1
C) Type checking	D) None of these		
20. Syntax trees are commonly used during		[	]
A) Syntax analysis	B) Lexical analysis	L	1
C) Semantic analysis	D) Both A & C		
21. The closure of L denoted as	D) bour A & C	г	1
A) $L^*$	B) L <sup>+</sup>	[	]
	D) $L^0$		
C) L* 22. L <sup>0</sup> is called	D) L	r	1
		[	]
A) Concatenation of Zero terms	B) Closure of zero terms		
C) Union of Zero terms	D) None	r	-
23. Which has highest precedence		[	]
A) *	B) Concatenation		
C)	D) All	_	_
24. Transition diagrams have collection of a no		[	]
A) Positions	B) States		
C) Stages	D) Edges		
25. The lexical analyzer tool is called		[	]
A) LUX	B) LEX		
C) FLEX	D) LES		
26. When several prefixes of the input match o	ne or more patterns	[	]
A) Always prefer longer prefix to a sho		-	-
B) Always prefer shorter prefix to a lo	-		
C) Both A&B			
D) None			
27. The latter file is compiled by the C compile	er into a file called	[	]

A) a out	B) aout		
C) A.Out	D) a.out		
28. The translation rules the form		[	1
A) Pattern {Action}	B) {Pattern} Action	L	L
C) Pattern {Action}	D) Pattern Action		
29. The set{0,1} is the	D) Futtern Herton	[	1
A) Decimal set	B) Octal set	L	L
C) Binary set	D) None		
30. The is a special character that can	,	[	1
A) Sentence	B) Word	L	L
C) Sentinels	D) Tokens		
31. Change in one variable to change another is	,	[	1
A) Aliasing	B) Changing	L	L
C) Differentiating	D) Renaming		
32. Parameters are passed from a calling proceed		[	1
A) Value	B) Reference	L	L
C) Both A&B	D) None		
33. Analysis portion of a compiler generally sep		ſ	]
A) Lexical analysis & parsing		-	L
C) Lexical analysis & syntax analysis	D) Lexical analysis & seman	ning ntic ana	lvsis
34. A is a description of form that the		[	]
A) Syntax	B) Procedure	L	L
C) Pattern	D) Function		
35 is any finite set of symbols		[	1
A) Strings	B) Characters	L	L
C) Alphabets	D) Numbers		
36 is example of alphabet used in s	,	[	1
A) C	B) D	L	L
C) ASCII	D) Both A & B		
37. Finite sequence of symbols is called		[	]
A) String	B) Words	L	L
C) Sentence	D) All the above		
38. If $x=dog y=house then xy=$		[	1
A) Dog House	B) doghouse	L	L
C) DOGHOUSE	D) DogHouse		
39. If x & y are strings, then the concatenation $\phi$		[	1
A) x*y	B) xy	L	L
C) XY	D) X*Y		
40. The Positive closure of L denoted as		[	1
A) L <sup>*</sup>	- B)L <sup>+</sup>	L	L
C) L*	$D) L^0$		
	,		

# <u>UNIT-2</u>

1. Context Free grammar production rule		[	]
A) a->b	B) A->b		
C) A->B	D) A-> $\alpha$	-	-
2. S->AB,A->a/b, B->b grammar can produce	-	l	J
A) b	B) ba		
C) AB	D) None	F	
3. Which one is not a LR(0) item		[	J
A) A->. Xyz	B) A->x.yz		
C) A->xyz	D) None	_	_
4. FIRST (a)		[	]
A) A	B) a		
C) Both A & B	D) None		
5. In LL(1) first L stands for		[	]
A) Left most derivation	B) Scanning from left to r	ight	
C) Both A & B	D) None		
6. The parsing table has no multiple entries is set	to be	[	]
A) LL(1)	B) LL(0)		
C) Predictive parser	D) Non recursive parser		
7. Follow(start symbol of grammar) should add _		[	]
A) +	B) a		
C) A	D) \$		
8. In shift action the input symbol is		[	]
A) Shifted to the stack	B) Reduced with non term	inal	1
C) Both A & B	D) None		
9. In synthesized attribute node value is calculate	,	]	1
A) Leaves to root	B) From top to bottom	L	L
C) Both A & B	D) None		
10. In Lex specifications the translation rules star		]	]
A) %,%	B) Begin, End	L	L
C) %%,%%	D) Start ,Stop		
11. A parser which is a variant of top-down parsi		ſ	]
A) Recursive Descend	B) Operator Precedence	L	L
C) LL (1) parser	D) LALR Parser		
12. The legal text which is derived from a disting		ſ	1
A) Axioms	B) lexemes	L	1
C) sentence symbol	D) both A & C		
13. A LL parser is also known as	D) bour M & C	[	]
A) Top down parser	B) Bottom up parser	L	1
C) LL(0)parser	D) LL(1)parser	г	1
14. Symbols that cannot be replaced are known a	B) Terminals	[	J
A) Non-terminals	,		
C) Symbols	D) tokens	г	г
15. Terminals represent character strings that are	•	[	J
A) Syntax analyser	B) lexical analyser		
C) semantic analyzer	D) none of these	-	-
16. What the letter 'T' represents in production $s_{1}$		[	]
A) Term	B) Token		

C) Table	D) None of these	_	-
17. Yaac is available as a command on		[	]
A) MNIX	B) UNIX		
C) DOS	D) None of these		
18. The process which starts from the leaf	node and ends with the s	starting s	ymbol is
known as		[	]
A) Top down parsing	B) Bottom up parsing		
C) Recursive parsing	D) LL(1)parser		
19. Which action in the shift reduce parsing dete	ect the syntax errors	[	]
A) ACTION	B) GOTO		
C) Error	D) Reduce		
20. The simplest method for shift reduced parse	,	[	]
A) SLR	B) LALR	L	-
C) CLR	D) LR		
21 is an attribute whose value at a node		terms of	attribute
at the parent and/or sibling of that node.	e în a parse dee îs defined în	r terms or	1
A) L-attribute	B) S-attribute	L	1
C) Synthesized	D) Inherited		
22. In shift action the input symbol is	D) Inferficed	Г	1
- ·	B) Reduced with non ter	L minol	]
A) Shifted to the stack $(x) = \sum_{n=1}^{\infty} (x + 1)^n (x$		mmai	
C) Both A & B	D) None	г	1
23 is a recursive descent parser that	-	L	]
A) Predictive Parser	B) LR		
C) Brute Force	D) Shift Reduce	_	
24. An attribute grammar in which all attributes	are then it is called S attrib	outed grai	
		L	]
A) Parsed	B) Inherited		
C) A-attributed	D) synthesized	F	7
25. The Output From second phase		Į	]
A) Parse tree	B) Intermediate Code		
C) Tokens	D) None	r.	-
26. The Output From last phase		[	]
A) Parse tree	B) Syntax tree		
C) Assembly language	D) Both A & B		
27. In parse tree the leaf node is labeled by		[	]
A) Epsilon (€)	B) Terminal		
C) Non terminal	D) Start symbol of gram	mar	
28. In top down parsers the parse tree constructed	ed from	[	]
A) Bottom to top	B) Top to bottom		
C) Both A & B	D) None		
29. E-> E*E consists		[	]
A) Left factoring	B) Left recursion		
C) Both A & B	D) None		
30. In LL(1) first L stands for	,	]	]
A) Left most derivation	B) Scanning from left to		L
C) Both A & B	D) None	0	
31. FIRST(+)		[	]
	B) +,-	L	Ţ
A) + C)+,-,*	D)None		

32. The parsing table has no multiple entries is	set to be	[	]
A) LL(1)	B) LL(0)	L	1
C) Predictive parser	D) Non recursive parser		
33. Which of the following is most powerful be	· •	[	]
A) SLR	B) LALR	L	1
C) CLR	D) Operator Precedence		
34. In Reduce action the input symbol is	_,	Γ	]
A) Shifted to the stack	B) Reduced with non term	inal	1
C) Both A & B	D) None		
35. LALR(1) grammar is	D) Hone	[	]
	) SLR(1)	L	1
C) LR(1)	D) None		
36. In bottom up parsing string is generated fro	,	ſ	1
A) RMD in reverse order	B) Leftmost derivation	L	1
C) Both A & B	D) None		
37. A parser which is a variant of top-down par	,	ſ	1
A) Recursive Descend	B) Operator Precedence	L	
C) LL(1) Parser	D) LALR Parser		
38. The legal text which is derived from a disti	,	ſ	1
A) Axioms	B) Lexemes	L	-
C) Sentence symbol	D) Both A & C		
39. A LL parser is also known as	,	[	1
A) Top down parser	B) Bottom up parser	L	-
C) LL(0)parser	D) LL(1)parser		
40. The end of file is represented by the special	· · · · · ·	[	1
A) \$	B)υ	-	-
C) μ	D) $\pi$		
-			

# <u>UNIT-3</u>

Advantage of panic mode of error recover A)It is simple to implement	B)It never gets into an in	finite loo	р
C)Both A) and B)	D) None of these		Ľ
2.An Intermediate code form is	,	[	]
A)Postfix notation	B)Syntax trees	Ľ	-
C)Three address code	D)All of these		
3.Intermediate code generation phase gets in		[	]
A)Lexical analyser	B) Syntax analyser	-	-
C)Semantic analyser	D)Error Handling		
4.Relocating bits used by relocating loader a		[	]
A)Relocating loader itself	B)Linker	-	-
C)Assembler	D)Macro Processor		
5.Reduction in strength means		[	]
A)Replacing run time computation b	by compile time computation	_	_
B)Removing loop invariant compute			
C)Removing common sub expressio			
D)None of these			
5. The computer languages are generally tra	inslated into	[	]
A) Assembly	B) Machine	_	_
-			
C) Pascal	D) FORTRAN		
		nt in a seq	uence
7. Any statement that immediately follows a		nt in a seq	uence
	goto or conditional goto statemen	nt in a seq [	uence ]
7.Any statement that immediately follows a hree address statements is a A) Leader		nt in a seg [	uence ]
7.Any statement that immediately follows a hree address statements is a A) Leader C) A& B	goto or conditional goto statemen B) Instructor D) none	[	]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> </ul>	goto or conditional goto statemen B) Instructor D) none	nt in a seq [ [	[uence ] ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statemen A)a:=b (op) c</li> </ul>	B) Instructor D) none t is B) a:=b c	[	]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statemen</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> </ul>	goto or conditional goto statemen B) Instructor D) none t is B) a:=b c D)None of these	[	]
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<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributes</li> </ul>	B) Instructor D) none t is B) a:=b c D)None of these tt is computed from the value of	[	]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statemen</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributes</li> </ul>	goto or conditional goto statemen B) Instructor D) none t is B) a:=b c D)None of these	[	]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributes</li> <li>b) S-attribute</li> <li>C) Inherited</li> </ul>	B) Instructor D) none t is B) a:=b c D)None of these ate is computed from the value of B) Synthesized D) All Above	[	] at the ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statemen</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attribute</li> <li>siblings and parent of that node.</li> <li>A) S-attribute</li> </ul>	B) Instructor D) none t is B) a:=b c D)None of these ate is computed from the value of B) Synthesized D) All Above	[ attributes	]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statemen</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributesiblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simulally attributes attribute</li></ul>	B) Instructor D) none t is B) a:=b c D)None of these te is computed from the value of B) Synthesized D) All Above Hated by B)LR Grammar	[ attributes [	] at the ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>O. The value of attributes</li> <li>Siblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simulated</li> </ul>	B) Instructor D) none t is B) a:=b c D)None of these tte is computed from the value of B) Synthesized D) All Above Hated by B)LR Grammar D) Ambiguous grammar	[ attributes [	] at the ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributes iblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simular</li> <li>A)LL grammar</li> <li>C) Operator grammar</li> </ul>	B) Instructor D) none t is B) a:=b c D)None of these tte is computed from the value of B) Synthesized D) All Above Hated by B)LR Grammar D) Ambiguous grammar	[ attributes [	] at the ] ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statemen</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributesiblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simulal A)LL grammar</li> <li>C) Operator grammar</li> <li>11.A Parse tree showing the values of attributes of attribu</li></ul>	B) Instructor D) none t is B) a:=b c D)None of these te is computed from the value of B) Synthesized D) All Above Hated by B)LR Grammar D) Ambiguous grammar utes at each node is called	[ attributes [	] at the ] ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributes iblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simular</li> <li>A)LL grammar</li> <li>C) Operator grammar</li> <li>11.A Parse tree showing the values of attribute</li> <li>A) Syntax</li> </ul>	B) Instructor D) none t is B) a:=b c D)None of these te is computed from the value of B) Synthesized D) All Above thated by B)LR Grammar D) Ambiguous grammar utes at each node is called B) Augmented D) Semantic	[ attributes [	] at the ] ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>O. The value of attributes iblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simulally attributes can be easily simulated a</li></ul>	B) Instructor D) none t is B) a:=b c D)None of these te is computed from the value of B) Synthesized D) All Above thated by B)LR Grammar D) Ambiguous grammar utes at each node is called B) Augmented D) Semantic	[ attributes [ [	] at the ] ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statemen</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributesiblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simulal A)LL grammar</li> <li>C) Operator grammar</li> <li>11.A Parse tree showing the values of attribute A) Syntax</li> <li>C) Annotated</li> <li>12. S-attribute definition is also called as</li> </ul>	B) Instructor D) none t is B) a:=b c D)None of these tte is computed from the value of B) Synthesized D) All Above talated by B)LR Grammar D) Ambiguous grammar utes at each node is called B) Augmented D) Semantic	[ attributes [ [	] at the ] ]
<ul> <li>7.Any statement that immediately follows a hree address statements is a</li> <li>A) Leader</li> <li>C) A&amp; B</li> <li>8.General Form of a three-address statement</li> <li>A)a:=b (op) c</li> <li>C) a:=b</li> <li>9. The value of attributes iblings and parent of that node.</li> <li>A) S-attribute</li> <li>C) Inherited</li> <li>10 Synthesized attributes can be easily simulally attributes can be easily simulated attributes can be easily simulated</li></ul>	goto or conditional goto statemen B) Instructor D) none t is B) a:=b c D)None of these ate is computed from the value of B) Synthesized D) All Above allated by B)LR Grammar D) Ambiguous grammar utes at each node is called B) Augmented D) Semantic B) Prefix SDT D)none	[ attributes [ [	] at the ] ]

	A)Name	B)Data types		
	C)Scopes, binding& life time D)Al	Above		
14	are the examples for high level Intermed	iate languages.	[	]
	A)Abstract Syntax Tree (AST)	B)Postfix notation		
	C) both A & B	D) none of these		
15. T	hree address code is alevel intermedia	ate language	[	]
	A) High	B) Medium		
	C) Low	D) none		
16	is the process of replacing a function ca		-	
	A) Inlining function	B) friend function	L	]
	C) both A & B	D) none		
17 Th	in intermediate optimized code can be sequen		[	]
1/.11	A) Quadruples	B)target code	L	1
	C)source program	D)binary language		
18 V	ariable descriptors are also known as		г	]
10. 1	A) register descriptor	B)address variables	L	1
	C) pseudo registers	D)constants		
19 Ar	n attribute at node N is defined only	,	's nar	ent
	and N's siblings.		5 par	]
nisen	A) synthesized	B) Inherited	L	Ţ
	i i j sjinnesizea	B) milerneu		
	C) A & B	D) None		
20.A	C) A & B graph depicts the flow of information	D) None a among the attribute instances in	n a mai	rticu
	graph depicts the flow of information	among the attribute instances ir	-	rticu
	graph depicts the flow of information tree.	among the attribute instances in [	-	rticu
	graph depicts the flow of information tree. A) dependency	a among the attribute instances in [ B) annotated parse	-	rticu
parse	graph depicts the flow of information tree. A) dependency C) syntax	a among the attribute instances in [ B) annotated parse D) none	]	
parse	graph depicts the flow of information tree. A) dependency C) syntax he fields of Triples are op,arg1 &	a among the attribute instances in [ B) annotated parse D) none	]	rticu ]
parse	graph depicts the flow of information tree. A) dependency C) syntax he fields of Triples are op,arg1 & A) Result	a mong the attribute instances in [ B) annotated parse D) none B) arg2	]	
parse 21.Th	graph depicts the flow of information tree. A) dependency C) syntax he fields of Triples are op,arg1 & A) Result C) operands	a among the attribute instances in [ B) annotated parse D) none	]	
parse 21.Th	graph depicts the flow of information tree. A) dependency C) syntax he fields of Triples are op,arg1 & A) Result C) operands ject program is a	a mong the attribute instances in [ B) annotated parse D) none B) arg2	]	
parse 21.Th	graph depicts the flow of information tree. A) dependency C) syntax he fields of Triples are op,arg1 & A) Result C) operands	a mong the attribute instances in [ B) annotated parse D) none B) arg2 D) none	]	
parse 21.Th	graph depicts the flow of information tree. A) dependency C) syntax he fields of Triples are op,arg1 & A) Result C) operands ject program is a A) Program written in machine language B) Program to be translated into machine language	a among the attribute instances in [ B) annotated parse D) none B) arg2 D) none	]	
parse 21.Th	graph depicts the flow of information tree. A) dependency C) syntax he fields of Triples are op,arg1 & A) Result C) operands ject program is a A) Program written in machine language	a among the attribute instances in [ B) annotated parse D) none B) arg2 D) none	]	
parse 21.Th 22.ob	graph depicts the flow of information tree. A) dependency C) syntax the fields of Triples are op,arg1 & A) Result C) operands ject program is a A) Program written in machine language B) Program to be translated into machine language C) Translation of high level language into the	a mong the attribute instances in [ B) annotated parse D) none B) arg2 D) none anguage. machine language.	]	]
parse 21.Th 22.ob	<ul> <li> graph depicts the flow of information tree.</li> <li>A) dependency</li> <li>C) syntax</li> <li>e fields of Triples are op,arg1 &amp;</li> <li>A) Result</li> <li>C) operands</li> <li>ject program is a</li> <li>A) Program written in machine language</li> <li>B) Program to be translated into machine la</li> <li>C) Translation of high level language into a D) none of these</li> </ul>	a among the attribute instances in [ B) annotated parse D) none B) arg2 D) none anguage. machine language.	]	
parse 21.Th 22.ob	<ul> <li> graph depicts the flow of information tree.</li> <li>A) dependency</li> <li>C) syntax</li> <li>e fields of Triples are op,arg1 &amp;</li> <li>A) Result</li> <li>C) operands</li> <li>ject program is a</li> <li>A) Program written in machine language</li> <li>B) Program to be translated into machine la</li> <li>C) Translation of high level language into r</li> <li>D) none of these</li> <li>hen is the type checking usually done?</li> </ul>	a among the attribute instances in B) annotated parse D) none B) arg2 D) none anguage. machine language. uring lexical analysis	]	]
parse 21.Th 22.ob	<ul> <li> graph depicts the flow of information tree.</li> <li>A) dependency</li> <li>C) syntax</li> <li>e fields of Triples are op,arg1 &amp;</li> <li>A) Result</li> <li>C) operands</li> <li>ject program is a</li> <li>A) Program written in machine language</li> <li>B) Program to be translated into machine la</li> <li>C) Translation of high level language into r</li> <li>D) none of these</li> <li>hen is the type checking usually done?</li> <li>A) During syntax directed translationB) Du</li> <li>C) During code optimization</li> </ul>	a mong the attribute instances in [ B) annotated parse D) none B) arg2 D) none anguage. machine language.	]	]
parse 21.Th 22.ob	<ul> <li> graph depicts the flow of information tree.</li> <li>A) dependency</li> <li>C) syntax</li> <li>e fields of Triples are op,arg1 &amp;</li> <li>A) Result</li> <li>C) operands</li> <li>ject program is a</li> <li>A) Program written in machine language</li> <li>B) Program to be translated into machine la</li> <li>C) Translation of high level language into a</li> <li>D) none of these</li> <li>hen is the type checking usually done?</li> <li>A) During syntax directed translationB) Dependent</li> </ul>	a among the attribute instances in B) annotated parse D) none B) arg2 D) none anguage. machine language. uring lexical analysis	] [ [ [	] ]
parse 21.Th 22.ob	graph depicts the flow of information tree. A) dependency C) syntax te fields of Triples are op,arg1 & A) Result C) operands ject program is a A) Program written in machine language B) Program to be translated into machine la C) Translation of high level language into the distribution of these hen is the type checking usually done? A) During syntax directed translationB) During code optimization mplicit type conversions, is	a among the attribute instances in [ B) annotated parse D) none B) arg2 D) none anguage. machine language. uring lexical analysis D) During syntax analysis	] [ [ [	] ]
parse 21.Th 22.ob 23.W 24. In	graph depicts the flow of information tree. <ul> <li>A) dependency</li> <li>C) syntax</li> <li>e fields of Triples are op,arg1 &amp;</li> <li>A) Result</li> <li>C) operands</li> <li>ject program is a</li> <li>A) Program written in machine language</li> <li>B) Program to be translated into machine la</li> <li>C) Translation of high level language into a</li> <li>D) none of these</li> <li>hen is the type checking usually done?</li> <li>A) During syntax directed translationB) Du</li> <li>C) During code optimization</li> <li>nplicit type conversions, is</li> <li>A)Done automatically by the compiler.</li> </ul>	a among the attribute instances in [ B) annotated parse D) none B) arg2 D) none anguage. machine language. uring lexical analysis D) During syntax analysis B) Done automatically by the D) None of these	] [ [ [	] ]
parse 21.Th 22.ob 23.W 24. In	<ul> <li> graph depicts the flow of information tree.</li> <li>A) dependency</li> <li>C) syntax</li> <li>e fields of Triples are op,arg1 &amp;</li> <li>A) Result</li> <li>C) operands</li> <li>ject program is a</li> <li>A) Program written in machine language</li> <li>B) Program to be translated into machine la</li> <li>C) Translation of high level language into n</li> <li>D) none of these</li> <li>hen is the type checking usually done?</li> <li>A) During syntax directed translationB) Du</li> <li>C) During code optimization</li> <li>nplicit type conversions, is</li> <li>A)Done automatically by the compiler.</li> <li>C) Done automatically by the OS</li> </ul>	a among the attribute instances in [ B) annotated parse D) none B) arg2 D) none anguage. machine language. uring lexical analysis D) During syntax analysis B) Done automatically by the D) None of these	] [ [ [ User	] ] ]

attribute at the parent and/or sibling of		[	]
A) L-attribute	B)S-attribute		
C) Synthesized	D) Inherited	1 0	
-	all attributes are then it is called	IS a	
grammar.		[	]
A) Parsed	B)Inherited		
C) A-attributed	D) synthesized		
28. An attribute grammar in which a	all the attributes are synthesized is called_		ttribute
grammar.		[	]
A) P	B)Q		
C) R	D) S		
29. A keeps the information ab	-	[	]
A) Register descriptor	B) address descriptor		
C) variable descriptor	D) none of these		
	fields are used to represent operands.	[	]
A) 1	B)2		
C) 3	D) 4		
31. Which of the following is an interr	mediate code form	[	]
A) Three address code	B) syntax tree		
C) parser	D) none of these.		
32.A parse tree showing the values of	its attribute is called	[	]
A)Dependency Graph	B)parse tree		
C) Annonated parse tree	D) None		
33. A Type name is		[	]
A)Type expression	B)Type Checking		
C) Backpatching	D) None		
34. Makelist(i) is a function of		[	]
A)Type expression	B)Type Checking		
C) Backpatching	D) None		
35. Syntax directed translation scheme	e is desirable because	[	]
A)It is based on the syntax			
B)It is easy to modify			
C)Its description is independent	nt of any implementation		
D)All of these			
36. If Conversion from one type to an	nother type is done automatically by the con-	mplie	r then,
is called		[	]
A)Implicit conversion	B)Coercions		
C) Both A & B	D) None of these		
37. The term environment in programm	ning language semantics is said as	[	]
A) Function that maps a name			
B)Function that maps a name t			
-	brage location to the value held there		

D)None of the above 38.A self relocating program is one which \_\_\_\_ 1 ſ A) cannot be made to execute in any area of storage other than the designated for it at the time of its coding or translation B) Consists of a program and relevant information for its relocation C) Can itself perform the relocation of its address sensitive protions D) All of the above 39.In a bottom up evaluation of a syntax direction definition, inherited attributes can ] [ A) Always be evaluated B) Be evaluated only if the definition is L –attributed C) Be evaluated only if the definition has synthesized attributes D) None of the above 40. Generation of intermediate code based on a abstract machine model is useful in compilers because ſ 1 A) it makes implementation of lexical analysis and syntax analysis easier B)syntax directed translation can be written for intermediate code generation. C)It enhances the portability of the front end of the compiler D)it is not possible to generate code for real machines directly from high level language programs

# <u>UNIT-4</u>

A)Absolute machine language       B) Re-locatable machine code         C) Assembly language       D) all of the above         3.The statement of the form a:=b is called a statement.       []]         A)Common       B) Copy         C) Induction Variable       D) Decode         4.To check whether a variable is exactly defined once or not is a check. []]       A) Uniqueness check       B) Flow of Control Check         C) name check       D) Above all       5.       is a Data Structure, which is used by compiler to keep track of information []         A) Lexical analyser       B) Symbol Table       (P)         C.) Semantic Table       D) Semantic Analyzer       6. A symbol is said to be if it has different meaning depending on its context or use.         A) Override       B)Overloaded       (P)         C) Overwrite       D) None       7.         7. The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       [] ]         A) Optimize to code       B) Expand the Code       C) Reporting Errors         A) Optimize the code       B) Expand the Code       C) Data intermixed with instruction         D) None       10.       A       can be visualized as a set of records in data structure.       []         A) Register Allocation <th></th> <th></th> <th>[</th> <th>]</th>			[	]
2. Which one of the following is an object code form?       []]         A) Absolute machine language       B) Re-locatable machine code         C) Assembly language       D) all of the above         3. The statement of the form a:=b is called a statement.       []]         A) Common       B) Copy         C) Induction Variable       D) Decode         4. To check whether a variable is exactly defined once or not is a check. []]       A) Uniqueness check       B) Flow of Control Check         C) name check       D) Above all       5       is a Data Structure, which is used by compiler to keep track of information []         A) Lexical analyser       B) Symbol Table       C) Semantic Table       D) Semantic Analyzer         6. A symbol is said to be if it has different meaning depending on its context or use.       []]         A) Override       B)Overloaded       C) Overwrite       D) None         7. The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       []]       A) Optimize the code       B) Expand the Code       []]         A) Optimize the code       B) Expand the Code       []]       A) Optimize the code       B) Frequency reduction         C) Reporting Errors       D) Above all.       []]       A) Register Allocation       B) Variable       []] <t< td=""><td>A)Linear list</td><td>B) Search tree</td><td></td><td></td></t<>	A)Linear list	B) Search tree		
A)Absolute machine language       B) Re-locatable machine code         C) Assembly language       D) all of the above         3.The statement of the form a:=b is called a	C) Hash Table	D) Self-organizing list		
C) Assembly language       D) all of the above         3.The statement of the form a:=b is called astatement.       []         A)Common       B) Copy         C) Induction Variable       D) Decode         4.To check whether a variable is exactly defined once or not is a check. []       A) Uniqueness check       B) Flow of Control Check         C) name check       D) Above all       5.	2. Which one of the following is an object code	form?	[	]
3.The statement of the form a:=b is called a	A)Absolute machine language	B) Re-locatable machine co	ode	
A)Common       B) Copy         C) Induction Variable       D) Decode         4.To check whether a variable is exactly defined once or not is a check. [ ]       A) Uniqueness check       B) Flow of Control Check         C) name check       D) Above all       5.	C) Assembly language	D) all of the above		
C) Induction Variable       D) Decode         4.To check whether a variable is exactly defined once or not is a check. [ ]         A) Uniqueness check       B) Flow of Control Check         C) name check       D) Above all         5 is a Data Structure, which is used by compiler to keep track of information [ ]         A) Lexical analyser       B) Symbol Table         C) Semantic Table       D) Semantic Analyzer         6. A symbol is said to be if it has different meaning depending on its context or use.         A) Override       D) None         7.The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       [ ]         A) Stack Allocation       B) Heap Allocation       [ ]         A) Optimize the code       B) Expand the Code       [ ]         C) Reporting Errors       D) Above all.       [ ]         9.Machine independent optimization is       [ ]       [ ]         A) Register Allocation       B) Frequency reduction       [ ]         C) both A & B       D) none       [ ]       [ ]         A)Symbol table       B) Variable       [ ]       [ ]         A) Register Allocation       D) none       [ ]       [ ]         A) Register Allocation       D) none       [ ]       [ ]	3. The statement of the form a:=b is called a	statement.	[	]
4.To check whether a variable is exactly defined once or not is a check. [       ]         A) Uniqueness check       B) Flow of Control Check         C) name check       D) Above all         5 is a Data Structure, which is used by compiler to keep track of information [       ]         A) Lexical analyser       B) Symbol Table         C) Semantic Table       D) Semantic Analyzer         6. A symbol is said to be if it has different meaning depending on its context or use.       [         A) Override       B)Overloaded         C) Overwrite       D) None         7.The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       [         A) Stack Allocation       B) Heap Allocation         C) Static Allocation       D) Dynamic Allocation         S.An optimized compiler can perform       [         A) Optimize the code       B) Expand the Code         C) Reporting Errors       D) Above all.         9.Machine independent optimization is can be visualized as a set of records in data structure.       [         A) Symbol table       B) Variable         C) both A & B       D) none         11.A hash function should produce the same hash value for two different keys then it is call	A)Common	B) Copy		
A) Uniqueness check       B) Flow of Control Check         C) name check       D) Above all         5 is a Data Structure, which is used by compiler to keep track of information []         A) Lexical analyser       B) Symbol Table         C) Semantic Table       D) Semantic Analyzer         6. A symbol is said to be if it has different meaning depending on its context or use.       []         A) Override       B)Overloaded         C) Overwrite       D) None         7. The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       []         A) Stack Allocation       B) Heap Allocation         C) Static Allocation       D) Dynamic Allocation         S. An optimize the code       B) Expand the Code         C) Reporting Errors       D) Above all.         9.Machine independent optimization is       []         A) Register Allocation       B) Frequency reduction         C) Data intermixed with instruction       D) None         10. A       can be visualized as a set of records in data structure.       []         A)Symbol table       B) Variable       []         C) both A & B       D) none       []         A)Sollisions       B) Heap allocation       []	C) Induction Variable	D) Decode		
C) name check       D) Above all         5 is a Data Structure, which is used by compiler to keep track of information []       A) Lexical analyser       B) Symbol Table         C) Semantic Table       D) Semantic Analyzer       6. A symbol is said to be if it has different meaning depending on its context or use.         []       A) Override       B)Overloaded       []         A) Override       D) None       7. The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       []         A) Stack Allocation       B) Heap Allocation       []         C) Reporting Errors       D) Above all.       9. An optimize the code         B) Expand the Code       C) Reporting Errors       D) Above all.       9. Anothine independent optimization is         9.Machine independent optimization is	4. To check whether a variable is exactly defined	d once or not is a cheo	ck. [	]
5 is a Data Structure, which is used by compiler to keep track of information [       ]         A) Lexical analyser       B) Symbol Table         C) Semantic Table       D) Semantic Analyzer         6. A symbol is said to be if it has different meaning depending on its context or use.       [         A) Override       B)Overloaded         C) Overwrite       D) None         7. The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       [         A) Stack Allocation       B) Heap Allocation         C) Static Allocation       D) Dynamic Allocation         S. An optimize the code       B) Expand the Code         C) Reporting Errors       D) Alove all.         9.Machine independent optimization is       [         A) Register Allocation       B) Frequency reduction         C) both A & B       D) none         11.A hash function should produce the same hash value for two different keys then it is call          A) collisions         B) Heap allocation       [         C) Stack allocation       D) none of these.	A) Uniqueness check	B) Flow of Control Check		
A) Lexical analyser       B) Symbol Table         C) Semantic Table       D) Semantic Analyzer         6. A symbol is said to beif it has different meaning depending on its context or use.       []]         A) Override       B)Overloaded         C) Overwrite       D) None         7. The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       []]         A) Stack Allocation       B) Heap Allocation         C) Static Allocation       D) Dynamic Allocation         Stack Allocation       D) Above all.         9. A) Optimize the code       B) Expand the Code         C) Reporting Errors       D) Above all.         9. Machine independent optimization is       []]         A) Register Allocation       B) Frequency reduction         C) Data intermixed with instruction       D) None         10. A       can be visualized as a set of records in data structure.       []         A)Symbol table       B) Variable       []         C) both A & B       D) none       []         A)collisions       B) Heap allocation       []         C) both A & B       D) none of these.       []         A)Symbol table       B) Heap allocation       []         A)collisions       B) Heap alloc	C) name check	D) Above all		
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A) Override       B)Overloaded         C) Overwrite       D) None         7.The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       []]         A) Stack Allocation       B) Heap Allocation         C) Static Allocation       D) Dynamic Allocation         C) Static Allocation       D) Dynamic Allocation         8.An optimized compiler can perform       []]         A) Optimize the code       B) Expand the Code         C) Reporting Errors       D) Above all.         9.Machine independent optimization is       []]         A) Register Allocation       B) Frequency reduction         C) Data intermixed with instruction       D) None         10. A       can be visualized as a set of records in data structure.       []]         A)Symbol table       B) Variable       []         C) both A & B       D) none       []         A)collisions       B) Heap allocation			-	
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7.The storage strategy in which activation record is maintained even after the execution of a procedure is completed.       []]         A) Stack Allocation       B) Heap Allocation         C) Static Allocation       D) Dynamic Allocation         8.An optimized compiler can perform       []]         A) Optimize the code       B) Expand the Code         C) Reporting Errors       D) Above all.         9.Machine independent optimization is       []]         A) Register Allocation       B) Frequency reduction         C) Data intermixed with instruction       D) None         10. A can be visualized as a set of records in data structure.       []]         A)Symbol table       B) Variable         C) both A & B       D) none         11.A hash function should produce the same hash value for two different keys then it is call	C) Overwrite	D) None		
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A) Stack Allocation       B) Heap Allocation         C) Static Allocation       D) Dynamic Allocation         8.An optimized compiler can perform       []]         A) Optimize the code       B) Expand the Code         C) Reporting Errors       D) Above all.         9.Machine independent optimization is       []]         A) Register Allocation       B) Frequency reduction         C) Data intermixed with instruction       D) None         10. A can be visualized as a set of records in data structure.       []]         A)Symbol table       B) Variable         C) both A & B       D) none         11.A hash function should produce the same hash value for two different keys then it is call			-	
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C) Reporting Errors       D) Above all.         9.Machine independent optimization is       []         A) Register Allocation       B) Frequency reduction         C) Data intermixed with instruction       D) None         10. A can be visualized as a set of records in data structure.       []         A)Symbol table       B) Variable         C) both A & B       D) none         11.A hash function should produce the same hash value for two different keys then it is call			L	L
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C) both A & B       D) none         11.A hash function should produce the same hash value for two different keys then it is call			L	L
11.A hash function should produce the same hash value for two different keys then it is call	•			
A)collisions       B) Heap allocation         C) Stack allocation       D) none of these.         12. Set of information constitute a record in dynamic allocation is called [       ]         A)Activation Record (AR)       B)frame.         C) both A & B       D) none of these			hen it i	s calle
A)collisions       B) Heap allocation         C) Stack allocation       D) none of these.         12. Set of information constitute a record in dynamic allocation is called [       ]         A)Activation Record (AR)       B)frame.         C) both A & B       D) none of these	11.11 hash function should produce the same ha	sh value for two unforche keys t		_
C) Stack allocationD) none of these.12. Set of information constitute a record in dynamic allocation is called []A)Activation Record (AR)B)frame.C) both A & BD) none of these	A) collisions	B) Hean allocation	L	1
12. Set of information constitute a record in dynamic allocation is called [       ]         A)Activation Record (AR)       B)frame.         C) both A & B       D) none of these	-	-		
A)Activation Record (AR)B)frame.C) both A & BD) none of these	-		Г	1
C) both A & B D) none of these	-		L	]
15. The process of replacing the costry instruction by cheaper one is called [		none of these		
	· · · · · · · · · · · · · · · · · · ·	on by abaanar one is called	r	1

A)Strength Reduction	B)Strength Increases		
C) Induction Variable	D) none		
14.A movement of data from memory to regist	ter or register to memory is consid	ered as	s
cost.		[	]
A) code	B) unit		
C) register	D) none		
15.SISD full form		[	]
A)Single Instruction Single Data	B)Set of Instruction Single	Data	
C) Single Instruction Set of Data	D) none of these.		
16. The information associated with operations	s is	[	]
A) Operators	B)Functions, Function arguing	ments	
C)Scope and visibility D	D) all above		
17. The rules of a language determine	ne which declaration of the name a	applies	when
the name appears in the text of a program.		[	]
A) Life time	B)Alias		
C) Scope	D) none		
18.A technique for improving the quality of	of a target code locally by example	mining	a sho
sequence of target instructions and replacing	with faster sequence is called	optir	nizatior
		[	]
A) Peephole	B) Procedural		
C) Flow graph	D) none		
19. Which one of the following is a symbol tab	ble attribute?	[	]
A) scope	B)Data types		
C) names	D)all above		
20.Replacing multiplication operator with add	ition operator is	[	]
A) Constant Folding	B)Reduction in Strength	-	-
C) Copy Propagation	D)None		
21.A Transformation is called if it can be	e performed in single basic block	[	]
A)Local Optimization	B)Global optimization	L	-
C)Both A&B	D)None		
22. The transformations that are applied on th		s [	1
A) Global Optimization	B) Local Optimization	- L	
C) Block Optimization	D) none		
23. Which is not an example for function-pres		[	]
A)Copy propagation	B)Flow of control	L	1
C)Constant folding	D)dead-code elimination		
24. Optimization is phase in compilat	-	[	]
A)1	B)2	L	J
C)5	D)2 D)4		
25. A:=B+C find use and definition variables		[	1
A)use=A,B,Def=C	B) use=A, Def= B,C	L	1
C) use=B,C ,Def=A	D) None		

		[	]
A)x for y	B)y for x		
C)x=y	D)none		
27.If the variable contain its value and us	ed subsequently then variable is said	to be	
		[	]
A)Live	B)Dead		
C)Alive	D)All		
28. Certain code moving outside of the lo	_	[	]
A)code notion	B)induction variable		
C)code motion	D)strength reduction		
29. Copy statement is		[	]
A)X=a+b	B)a[i]=x		
C)x++	D)None		
30. Busy expressions are useful in perform	ming optimization	[	]
A)local	B)global		
C)loop	D)code movement		
31. The process of eliminating the repeate	d statements in intermediate code	[	]
A)Code optimization	B)Code generation		
C)Efficiency	D)implementation		
32. One of the purposes of using intermed	diate code in compilers is to	[	]
A) make parsing and semantic and	alysis simpler.		
B) improve error recovery and err	or reporting		
C) increase the chances of reusing	g the machine-independent code optin	nizer in (	other
compliers.			
D) improve the register allocation			
33. Which of the following statements are	e CORRECT?	[	]
1) Static allocation of all data area	as by a compiler makes it impossible t	to imple	ment
recursion.			
2) Automatic garbage collection is	s essential to implement recursion.		
3) Dynamic allocation of activation	on records is essential to implement re	cursion	•
4) Both heap and stack are essenti	al to implement recursion.		
A) 1 and 2 only	B) 2 and 3 only		
C) 3 and 4 only	D) 1 and 3 only		
34. Which one of the following is not an	addressing mode?	[	]
A) Register indirect	B)Auto increment		
C)Relative indexed	D)Immediate operand		
		[	]
35. Computers can have instruction forma		-	_
*	lress instructions		
35. Computers can have instruction formation A)Only two address and three address and three address and two address address and two address			

36. The identification of common sub expressio	n and replacement of run time co	mputa	tion by
compile- time computation is		[	]
A)loop optimization	B) local optimization		
C) constant folding	D) data flow analysis		
37. The graph that shows basic blocks and their	successor relationship is called	[	]
A) DAG	B) Flow graph		
C) Control graph	D) Hamiltonion graph		
38. The specific task storage manager performs		[	]
A) allocation/deallocation			
B) Protection of storage area allocated to	a program from illegal access b	y other	r
programs in the system			
C) the status of each program			
D) both A & B			
39. Pick the machine independent phase of the compiler			]
A)Syntax analysis	B) Lexical analysis		
C) Intermediate code generation	D) all of the above		
40. Type checking is normally done during		[	]
A) Lexical Analysis	B) Syntax Analysis		
C) Syntax Directed Translation	D) Code generation		

## <u>UNIT-5</u>

Determining Common sub expression can be do	one using	Г	1
A)Compiler	B) Interpreter	l	J
C)DAG	D) parse tree		
	7 1	г	1
2. In DAG the interior nodes are labeled with	D) Smarial colors	[	]
A) Number in BFS	B) Special colors		
C) Identifier	D) Number in BFS	1 5	1
3.A occurs when there is a reference to sto	-	a. [	]
A)Dangling reference	B) if-else		
C) Register allocation	D) none		
4. The process of moving the statement from one	part of the program to another	is calle	
		L	]
A) Code motion	B) Constant folding		
C) Copy propagation	D) none		
5 is a simple, systematic technique for all	,	g regist	er
pills.	6 6 6	[	]
A) DAG	B) Graph coloring	L	1
C) A & B	D) none		
5. The is a node in the flow graph, which	,	the loor	<b>`</b>
. The is a node in the now graph, which	precedes an the statements in	r Inc Ioop	,. ]
A) Numerator	B)Dominator	L	1
C) tree	D)none		
	D)hohe	г	1
7. Output of code generator is		[	]
A)Source code	B)Intermediate code		
C)Assembly code	D)None of these	л л	
3. At a point in a program if the value of the varia	able can be used subsequently,	then the	
variable is Variable.		L	]
A)Live	B)Dead		
C)Loop	D)None of these		
O.The process of assigning load addresses to the		d adjus	ting th
code and data in the program to reflect the assign	ed addresses is called _	[	]
A) Assembly	B) Parsing		
C) Relocation	D) Symbol resolution		
0. The runtime environment of the language mus	st be able to collect the unused	memory	у
utomatically for avoiding the memory leak, which	ch is called	[	]
A) Garbage collection	B) Memory allocation		
C) Optimization	D) none		
1decides whether the variable is all	ive at that point	[	]
A)Reaching definitions	B)Available Expressions	L	
C)Live Variables	D) None		
2. In Reachable Definition the output of the block	,	1 :	1
A)Spawn[B] U (Input[B] – Delete[B])	B) Spawn[B] U Input[B]	L	Ţ
C) Spawn[B] UDelete[B]	D) none of these.		
3. The transformations that are applied across the	·	г	1
**		L	
A) Global Optimization	B) Local Optimization		
C) Block Optimization	D) none		
4is the portion of the program which will no			

QUESTION BANK 2016 ] [ B) Dead Code C) reachable Code D) none of these 15Acronym for DAG \_\_\_\_\_ ſ ] A) Directed Acyclic Graph B)Direct Cyclic Graph D)Deviated Acyclic Graph C)Derived Acyclic Graph 16.In DAG the interior nodes are labeled by \_\_\_\_\_ symbol [ ] B) operator D) none of these. 17. The first statement in basic block is\_\_\_\_ [ ] B)Follow A) Main Statement D)Leader

A) Main Statement	D)FUIIOW		
C)Header	D)Leader		
18.Running time of a program depends on		[	]
A)The way the register and addressi			
B)The Order in which computations	are performed		
C)The usage of machine idioms			
D) All of these			
19. Which of the following does not interrup	ot a running process?	[	]
A)A device	B)Timer		
C)Scheduler	D)Power failure		
20. The optimization technique which is typ	ically applied on loops is	[	]
A) Peephole optimization	B) Removal of invariant co	mputat	ion
C)Constant folding	D)All of these		
21. The optimization which avoids test at ev		[	]
A)Loop Unrolling	B)Loop jamming		
C)Constant folding	D)None of these		
22. We can optimize code by		[	]
A)Dead code elimination	B)Common Subprograms		
C)Copy intermediate loop	D)Loop Declaration		
23.Input to code generator is		[	]
A)Source code	B)Intermediate code		
C)Target code	D)All of the above		
24.Local and loop optimization in turn prov		[	]
A)Data flow analysis	B)Constant folding		
C)Peephole optimization	D)DFA and Constant foldi	ng	
25. A Symbol table is		[	]
A)Data structure	B)Variable		
C)Data Type	D)None		
26.Live variables are used in eliminat	tion	[	]
A)Common sub Expression	<b>B</b> )Copy Propagation		
C)Code Motion	D)Dead code		
27.DAG is constructed from		[	]
A)3 address code	B)program		
C)blocks	D)none		
28.An estimate of how frequently a variable	e used is	[	]
A)Usage count	B)Reference count		
C)Program count	D)Process count		
29.A flow graph is a directed graph in whic	h the flow control information is add	ded to t	he
		[	]

**COMPILER DESIGN** 

A) Live code

A) Operands

C) both A & B

A)blocks	B)graph		
C)tree	D)basic blocks	-	-
30. Code generation phase converts the _		L	]
A) Intermediate optimized code	B) assembly code		
C) target code	D) none of these	-	-
31. Peep-hole Optimization is a form of		[	]
A) Local Optimization	B)Constant Folding		
C)Copy Propagation	D)None of these		
32. If the value of the variable is changed		s [	]
A)invarient variable	B)Dead variable		
C)Live variable	D)Induction variable		
33. Basic block is Sequence of		[	]
A)Statements	B)Loops		
C)Values	D)None		
34technique performed on target	code	[	]
A) Local Optimization	B)Loop optimization		
C)Peep-hole Optimization	D)None		
35. The strength reduction is related to		[	1
A)variables	B)Loops	L	
C)operators	D)All		
36. The local optimization performed its	·	as	
r r r	I I I I I I I I I I I I I I I I I I I	[	_1
A)Local scope	B)Global scope	L	
C)Dynamic scope	D)static scope		
37.Graph coloring is strategie of	· •	[	]
A)Register allocation	B)Heap allocation	L	1
C) Stack allocation	D)None		
38 keeps frequently used value in		[	]
A) Local register allocation	B) global register allocation	L	1
C) static allocation	D) none of these		
39. DAG representation of a basic block	·	[	]
A) Automatic detection of local c		L	1
B) Automatic detection of inducti			
C) Automatic detection of loop va	arrant		
D) None of the above	novide metivation for	г	1
40. Local and loop optimization in turn p		[	]
A) Data flow analysis	B) Constant folding		
C) Pee hole optimization	D) DFA and constant folding	5	