

## FACULTYPROFILE

1. Name : U.SRINIVASULU
2. Designation : Assistant Professor
3. Date of Birth : 15-05-1984
4. Qualification : M. Tech
5. Specialization : VLSI SYSTEM DESIGN
6. Date of joining : 01-06-2016
7. Academic Performance (Descending Order)



S.No	Degree/ Specialization	University/ Institution	Year of Pass	% of Marks	Class
1	Ph. D	--	--	--	--
2	M.Tech(VLSISD)	JNTUA	2010	73.33	Distinction
3	B.Tech(ECE)	JNTUH	2007	58.09	Second

8. Total Years of teaching/ industry experience before joining this college (in the descending order)

S. No.	Period		Organization / Institution	Position Held
	From	To		
1	31-07-2014	30-05-2016	GKCE,Sullurupet	Asst.Prof
2	01-02-2014	30-07-2014	BCEN,Nellore	Asst.Prof
3	27-06-2011	30-01-2014	MeRITS,Udayagiri	Asst.Prof

9. Achievements:

S.No	Achievements	Particulars
1	List of papers published	
2	Papers presented in conferences	1
3	Projects guided	10 UG
4	Industrial Consultancy	--
5	Books Published	---
6	FDPs/STTPs Organized	2
7	National /International Conferences Organized	--
8	Sponsored and consultancy projects	--
9	Short-term course attended	10