FACULTYPROFILE

1. Name : G.Erna

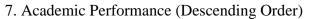
2. Designation : Associate Professor

3. Date of Birth : 03-05-1979

4. Qualification : M. Tech

5. Specialization : VLSI and System Design

6. Date of joining : 03/06/2017



S.No	Degree/speciliazation	University/ Institution	Year of Pass	% of Marks	Class
1	Ph. D(Pursuing)	Pondicherry	-		
		University			
2	M.Tech/VLSI	JNTUH	2008	65	First
	&System Design				
3	B.Tech/ECE	JNTH	2003	52	Second

8. Total Years of teaching/ industry experience before joining this college (in the descending order)

S. No.	Period		Organization /	Position Held	
	From	To	Institution		
1	02-11-2007	30-06-2009	VIST,Hyderabad	Asst.Prof	
2	2 02-06-2009 30-06-20		Paul Raj Engineering	Asst.Prof	
2	02 00 200)	30 00 2010	College,Bhadrachalam	71351.1101	
3	02-07-2010	30-05-2018	MIST,Sathupalli	Asst.Prof	

9. Achievements:

S.No	Achievements	Particulars	
1	List of papers published	4	
2	Papers presented in conferences	3	
3	Projects guided	16 UG & 4 PG	
4	Industrial Consultancy		
5	Books Published		
6	FDPs/STTPs Organized		
7	National /International Conferences	3 Conference	
	Organized	3 Conference	
8	Sponsored and consultancy projects	Nil	
9	Short-term course attended	4	

