SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY: PUTTUR
(AUTONOMOUS)

4th BoS Meeting of Computer Science & Information Technology (CSIT)

Date: 14/08/2019

The 4th meeting of Board of Studies (BoS) in Computer Science & Information Technology is held on 14th August, 2019 (Wednesday) at 2.00 PM in the Department of Computer Science & Information Technology, Siddharth Institute of Engineering & Technology, Puttur, Chittoor – Dist.

As per the UGC (University Grant Commission) guidelines, the Choice Based Credit System (CBCS) and electives have been implemented in the curriculum.

Dr. P. Ramesh babu, Chairman - BoS chaired the meeting and welcomed all the members to the fourth BoS meeting and discussed about the following agenda:

1. Approval of course structure for I year UG in CSIT w.e.f., A.Y. 2019-2020.
2. Approval of syllabi for I year UG in CSIT w.e.f., A.Y.2019-2020.
3. Approval of Panel of Question Paper setters.
4. Approval of Panel of Examiners.
5. Any other item.

After a brief introduction of the agenda items listed above, each agenda item were taken up for discussion and the following resolutions were passed.

Minutes:

Agenda: 1

Approval of course structure for I year UG in CSIT w.e.f.,A.Y.2019-2020.

Resolution: 1

After detailed discussion the BoS resolved to approve the course structure for I year UG (given in **Annexure –I**) applicable from the A.Y.2019-2020.

Agenda: 2

Approval of syllabi for I year UG in CSIT w.e.f., A.Y.2019-2020.

Resolution: 2

After the thorough discussion syllabus was framed to make the students acquire the required technical knowledge and skills. The BoS resolved to approve the syllabi framed for the I Year B.Tech I & II-Semesters (given in **Annexure-II**).

A. Course & Syllabus Comparison

With reference to the R18 regulations, the new regulation (R19) syllabus for Ist year has the following modifications, which are given in the below table.

I B.Tech

S.No	R18 Regulation	R19 Regulation	Percentage of course content changed
1	Mathematics-1	Algebra and Calculus	60
2	Chemistry	Applied Chemistry	40
3	Engineering Graphics & Design	Engineering Graphics	10
4	English	Communicative English	10
5	Chemistry Lab	Applied Chemistry Lab	0
6	English Lab	Communicative English Lab	0
7	Workshop Practices Lab	Workshop Practices Lab	0

8	Mathematics-II		0
9	Semi-Conductor Physics	Semiconductor Physics	0
10	Programming for Problem Solving		63
11	Digital Logic Design	Digital Logic Design	0
12	Basic Electrical Engineering	Basic Electrical & Electronics Engineering	20
13	Programming for Problem Solving Lab	C and Data Structures Lab	85.7
14	Physics Lab	Semiconductor Physics Lab	0
15	Essence of Indian Traditional Knowledge	Essence of Indian Traditional Knowledge	0
16		Python Programming	100
17		Python Programming Lab	100

Consolidated Sheet

Course	Total courses	Percentage of syllabus changed
CSIT B.Tech I Year	17	32.58

B. Course Relevance

The courses that comes under the category of Employability, Skill or Entrepreneurship development are shown in the table below.

Sno	Course Title	Course Code	Relevance
1	Algebra and Calculus	19HS0830	Skill Development
2	Engineering Graphics	19ME0302	Skill Development
3	Python Programming	19CS0501	Skill Development
4	Python Programming Lab	19CS0502	Skill Development
5	Workshop Practices Lab	19ME0301	Skill Development
6	Probability & Statistics	19HS0835	Skill Development
7	Communicative English	19HS0810	Skill Development

8	Basic Electrical & Electronics Engineering	19EE0240	Skill Development
9	Digital Logic Design	19CS0503	Skill Development
10	Communicative English Lab	19HS0811	Skill Development
11	Essence of Indian Traditional Knowledge	19HS0817	Skill Development

As per the decisions taken in the meeting and considering the suggestions from the suggestions or feedback from the stakeholders for the above modifications in the curriculum.

Agenda: 3

Approval of Panel of question paper setters

Resolution: 3

Approved the panel of question paper setting (given in **Annexure –III**) to be submitted to the college academic council for approval.

Agenda: 4
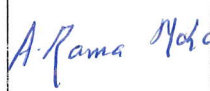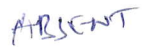
Approval of Panel of examiners
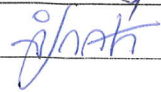
Resolution: 4

Approved the panel of examiners prepared for valuation (given in **Annexure –IV**) to be submitted to the college academic council for approval.

The above items were discussed, debated and the necessary approval was accorded by the BoS. The meeting was concluded with vote of thanks proposed by the Chairman-BoS.

Members Present

S. No	Member Name	Designation/ Organization	Role of BoS	Signature
1.	Dr. P. Ramesh Babu	Professor & HOD-SIETK	Chairman	
2.	Dr. D. William Albert	Professor – SIETK	Member	
3.	Mr. P. Balaji	Associate Professor – SIETK	Member	
4.	Mrs. R. M. Mallika	Associate Professor – SIETK	Member	
5.	Mr. D. Sainath	Assistant Professor – SIETK	Member	
6.	Dr. D. Rajyalakshmi	Professor, Department of Computer Science & Engineering, JNTUK, Kakinada. Contact No.9618016555 E-mail : rajyalakshmi.cse@jntukucev.ac.in	Member	
7.	Dr. A. Rama Mohan Reddy	Professor, Department of Computer Science & Engineering, SVU College of Engineering, S.V. University, Tirupati – 517502. Contact No: 9849045025 Email: ramamohansvu@yahoo.com	Member	A. Rama Mohan Reddy
8.	Dr. Ch. Sudhakar	Associate Professor, Dept. of CSE, NIT – Warangal Contact No: 9840370425 Email: chapram@nitw.ac.in	Member	
9.	Mr. D. Sriramdeshpande	Senior Project Engineer, Smart Enovation India Limited, Bangalore, Karnataka. Contact No. 8861345177, 9960506473 Email:sriram.d@smartenovations.com sriramdeshpande@gmail.com	Member	ABSENT
10.	V. Arun kumar	Technical Lead, HCL Technologies, Chennai. Contact No. 9176932956, Email: arunkumar_v12@yahoo.co.in	Member	ABSENT